

Pages 1 to 18

CAPACITORS, FIXED, CHIPS, CERAMIC DIELECTRIC, TYPE I, BASED ON TYPE 2220

ESCC Detail Specification No. 3009/006

ISSUE 4 October 2010





PAGE

ISSUE

LEGAL DISCLAIMER AND COPYRIGHT

European Space Agency, Copyright © 2010. All rights reserved.

The European Space Agency disclaims any liability or responsibility, to any person or entity, with respect to any loss or damage caused, or allleged to be caused, directly or indirectly by the use and application of this ESCC publication.

This publication, without the prior permission of the European Space Agency and provided that it is not used for a commercial purpose, may be:

- copied in whole in any medium without alteration or modification.
- copied in part, in any medium, provided that the ESCC document identification, comprising the ESCC symbol, document number and document issue, is removed.



PAGE 2

ISSUE 4

DOCUMENTATION CHANGE NOTICE

(Refer to https://escies.org for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
605	Specification upissued to incorporate technical and editorial changes per DCR.
an en	



PAGE 3

ISSUE 4

TABLE OF CONTENTS

1.	<u>GENERAL</u>	Page 5
4.4	Conn	5
1.1	Scope Rence of Components	5
1.2	Range of Components	5
1.3	Maximum Ratings	5
1.4	Parameter Derating Information	5
1.5 1.6	Physical Dimensions Functional Diagram	5
1.0	Pulictional Diagram	5
2.	APPLICABLE DOCUMENTS	8
3.	TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS	8
4.	REQUIREMENTS	8
4.1	General	8
4.2	Deviations from Generic Specification	8
4.2.1	Deviations from Special In-process Controls	8
4.2.2	Deviations from Final Production Tests	8
4.2.3	Deviations from Burn-in and Electrical Measurements	8
4.2.4	Deviations from Qualification Tests	8
4.2.5	Deviations from Lot Acceptance Tests	8
4.3	Mechanical Requirements	8
4.3.1	Dimension Check	8
4.3.2	Weight	9
4.3.3	Adhesion	9
4.4	Materials and Finishes	9
4.4.1	Terminations	9
4.5	Marking	9
4.5.1	General	9
4.5.2	The ESCC Component Number	9
4.5.3	Electrical Characteristics and Ratings	10
4.5.4	Traceability Information	11
4.6	Electrical Measurements	11
4.6.1	Electrical Measurements at Room Temperature	11
4.6.2	Electrical Measurements at High and Low Temperatures	11
4.6.3	Circuits for Electrical Measurements	11
4.7	Burn-in Tests	11
4.7.1	Parameter Drift Values	11
4.7.2	Conditions for Burn-in	11
4.7.3	Electrical Circuit for Burn-in	11
4.8	Environmental and Endurance Tests	14
4.8.1	Measurements and Inspections on Completion of Environmental Tests	14
4.8.2	Measurements and Inspections at Intermediate Points during Endurance Tests	14
4.8.3	Measurements and Inspections on Completion of Endurance Tests	14
4.8.4	Conditions for Operating Life Test	14
4.8.5	Electrical Circuit for Operating Life Tests	14



PAGE 4 ISSUE 4

		Page
TABLE	<u>s</u>	
1(a)	Range of Components	6
1(b)	Maximum Ratings	7
2	Electrical Measurements at Room Temperature	12
3	Electrical Measurements at High and Low Temperatures	12
4	Parameter Drift Values	13
5	Conditions for Burn-in and Operating Life Tests	13
6	Measurements and Inspections on Completion of Environmental Tests and at Intermediate Points and on Completion of Endurance Testing	15
FIGUR	r <u>es</u>	
1	Parameter Derating Information	7
2	Physical Dimensions	7
3	Functional Diagram	7
4	Test Circuits	13
5	Electrical Circuit for Burn-in and Operating Life Tests	13
APPEN	NDICES (Applicable to specific Manufacturers only)	
'A'	Agreed Deviations for Vitramon Ltd. (UK)	17
'B'	Agreed Deviations for AVX/TPC (F)	18



PAGE

ISSUE 4

5

1. GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics, test and inspection data for Capacitors, Fixed, Chips, Ceramic Dielectric, Type I, based on Type 2220. It shall be read in conjunction with ESCC Generic Specification No. 3009, the requirements of which are supplemented herein.

1.2 RANGE OF COMPONENTS

The range of capacitors covered by this specification is given in Table 1(a).

1.3 MAXIMUM RATINGS

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the capacitors specified herein, are as scheduled in Table 1(b).

1.4 PARAMETER DERATING INFORMATION (FIGURE 1)

Not applicable.

1.5 PHYSICAL DIMENSIONS

The physical dimensions of the capacitors specified herein are shown in Figure 2.

1.6 FUNCTIONAL DIAGRAM

The functional diagram for the capacitors specified herein is shown in Figure 3.



PAGE 6

ISSUE 4

TABLE 1(a) - RANGE OF COMPONENTS

CAPACITANCE RANGE (pF)	TOLERANCE (±) (%)	VALUES SERIES	RATED VOLTAGE (U _R) (V)
464 to 33200	1.0	E 96	100
464 to 33200	2.0	E 48	100
470 to 33000	5.0	E 24	100
470 to 33000	10	E 12	100
464 to 47500	1.0	E 96	50
464 to 48700	2.0	E 48	50
470 to 47000	5.0	E 24	50
470 to 47000	10	E 12	50
464 to 56200	1.0	E 96	25
464 to 56200	2.0	E 48	25
470 to 56000	5.0	E 24	25
470 to 56000	10	E 12	25
464 to 68100	1.0	E 96	16
464 to 68100	2.0	E 48	16
470 to 68000	5.0	E 24	16
470 to 68000	10	E 12	16

NOTES

1. As specified in Para. 4.4.1 and Figure 2, these ranges are available in 6 variants.



PAGE 7

ISSUE 4

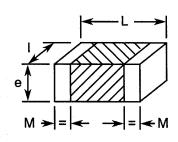
TABLE 1(b) - MAXIMUM RATINGS

No.	CHARACTERISTICS	SYMBOL	LIMITS		UNIT	REMARKS	
			MIN.	MAX.	ONIT	NEWARKO	
1	Rated Voltage	U _R	See Ta	ble 1(a)	V	-	
2	Operating Temperature Range	T _{amb}	– 55	+ 125	°C	Without derating	
3	Storage Temperature Range	T _{stg}	- 55	+ 125	°C	-	
4	Maximum Soldering Temperature	T _{sol}	- .	+ 260	°C	Soldering time: t: <10 sec.	

FIGURE 1 - PARAMETER DERATING INFORMATION

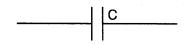
Not applicable.

FIGURE 2 - PHYSICAL DIMENSIONS



	DIMENSIONS (mm)				
SYMBOL	VARIANTS 01, 03, 06		VARIANTS 02, 04,		
	MIN.	MAX.	MIN.	MAX.	
L	5.2	6.2	5.2	6.7	
l	4.5	5.5	4.5	6.0	
е	-	1.8	-	2.3	
М	0.2	0.75	0.2	0.75	

FIGURE 3 - FUNCTIONAL DIAGRAM





PAGE

8

ISSUE 4

2. APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:-

(a) ESCC Generic Specification No. 3009 for Capacitors, Fixed, Chips, Ceramic Dielectric, Types I and II.

3. TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply. In addition, the following symbols are used:-

 V_T = Test Voltage.

4. **REQUIREMENTS**

4.1 GENERAL

The complete requirements for procurement of the capacitors specified herein shall be as stated in this specification and ESCC Generic Specification No. 3009 for Capacitors, Fixed, Chips, Ceramic Dielectric, Types I and II. Deviations from the Generic Specification, applicable to this specification only, are listed in Para. 4.2.

Deviations from the applicable Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

4.2 DEVIATIONS FROM GENERIC SPECIFICATION

4.2.1 <u>Deviations from Special In-process Controls</u>

None.

4.2.2 Deviations from Final Production Tests (Chart II)

(a) Para. 9.2.1, Drying: Shall not be performed.

4.2.3 <u>Deviations from Burn-in and Electrical Measurements (Chart III)</u>

None.

4.2.4 Deviations from Qualification Tests (Chart IV)

None.

4.2.5 Deviations from Lot Acceptance Tests (Chart V)

None.

4.3 MECHANICAL REQUIREMENTS

4.3.1 Dimension Check

The dimensions of the capacitors specified herein shall be verified in accordance with the requirements set out in Para. 9.3 of ESCC Generic Specification No. 3009 and shall conform to those shown in Figure 2 of this specification.



PAGE

9

ISSUE 4

4.3.2 Weight

The maximum weight of the capacitors specified herein shall be 0.3 grammes.

4.3.3 Adhesion

The requirements for adhesion are specified in Para. 9.5 of ESCC Generic Specification No. 3009.

4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the capacitors specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material does not guarantee acceptance of the finished product.

4.4.1 Terminations

- Variant 01: The capacitors shall be terminated with AgPd pads.
- Variant 02: The capacitors shall be terminated with AgPd with solder coating, 62 Sn, 36 Pb, 2.0 Ag %, +188°C.
- Variant 03: The capacitors shall be terminated with AgPdPt pads.
- Variant 04: The capacitors shall be terminated with AgPdPt with solder coating, 62 Sn, 36 Pb, 2.0 Ag %, +188°C.
- Variant 05: The capacitors shall be terminated with Ag, Ni barrier with solder coating, 62 Sn, 36Pb, 2.0 Ag %, +188°C.
- Variant 06: The capacitors shall be terminated with Ag, Ni barrier with coating tin-lead, near eutectic, minimum 10% lead.

All the above Variants are suitable for reflow soldering.

N.B.

Variant 06 is the preferred termination finish for the specified chip size (see Figure 2).

4.5 MARKING

4.5.1 General

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and the following paragraphs.

These components being too small to accommodate the marking as specified hereafter, the marking information in full shall accompany each component in its primary package. Such marking shall comprise:-

- (a) The ESCC Component Number.
- (b) Characteristics and Ratings.
- (c) Traceability Information.

4.5.2 The ESCC Component Number

Each component shall bear the ESCC Component Number which shall be constituted and marked as follows:

	300900601B
Detail Specification Number	
Type Variant (see Para. 4.4.1 and Figure 2)	
Testing Level (B or C, as applicable)	and the second s



PAGE 10

ISSUE 4

4.5.3 <u>Electrical Characteristics and Ratings</u>

The electrical characteristics and ratings to be marked in the following order of precedence are:-

- (a) Capacitance Value.
- (b) Tolerance.
- (c) Rated Voltage.

The information shall be constituted and marked as follows:-

			22023A
Capacitance Value (22 000pF)			
Tolerance (±5.0%)		The state of the s	· · · · · · · · · · · · · · · · · · ·
Rated Voltage (25V)	 		

4.5.3.1 Capacitance Values

The capacitance values shall be expressed by means of the following codes. The unit quantity for marking shall be picofarads (pF).

CAPACITANCE VALUE	CODE
XXX	XXX0
XXX10 ¹	XXX1
XXX10 ²	XXX2

4.5.3.2 Tolerances

The tolerances on capacitance values shall be indicated by the code letters specified hereafter.

TOLERANCE (%)	CODE LETTER
± 1.0	F
± 2.0	G
± 5.0	J
± 10	K

4.5.3.3 Rated Voltage

The rated voltages shall be indicated by the code letters specified hereafter.

RATED VOLTAGE (U _R) (V)	CODE LETTER
16	X
25	Α
50	С
100	Е



PAGE 11

ISSUE 4

4.5.4 <u>Traceability Information</u>

Traceability information shall be marked in accordance with the requirements of ESCC Basic Specification No. 21700.

- (a) Manufacturing Date Code.
- (b) Manufacturer's Name.

4.6 <u>ELECTRICAL MEASUREMENTS</u>

4.6.1 Electrical Measurements at Room Temperature

The parameters to be measured at room temperature are scheduled in Table 2. Unless otherwise specified, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.6.2 Electrical Measurements at High and Low Temperatures

The parameters to be measured at high and low temperatures are scheduled in Table 3.

4.6.3 Circuits for Electrical Measurements

A circuit for use in performing the electrical measurements listed in Table 2 of this specification is shown in ESCC Generic Specification No. 3009.

4.7 BURN-IN TESTS

4.7.1 Parameter Drift Values

The parameter drift values applicable to burn-in are specified in Table 4 of this specification. Unless otherwise stated, measurements shall be performed at T_{amb} = +22±3 °C. The parameter drift values (Δ) applicable to the scheduled parameters shall not be exceeded. In addition to these drift value requirements for a given parameter, the appropriate limit value specified in Table 2 shall not be exceeded.

4.7.2 <u>Conditions for Burn-in</u>

The requirements for burn-in are specified in Section 7 of ESCC Generic Specification No. 3009. The conditions for burn-in shall be as specified in Table 5 of this specification.

On completion of burn-in, a recovery period of 24 ± 2 hours is necessary before performance of the end-measurements.

4.7.3 Electrical Circuit for Burn-in (Figure 5)

Not applicable.



PAGE 12

ISSUE 4

TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE

No.	CHARACTERISTICS SYMBOL	evmpol .	ESCC 3009 TEST	LIMITS		UNIT
		CONDITIONS	MIN.	MAX.		
1	Capacitance	С	Para. 9.4.1.1	See Table 1(a)		-
2	Tangent of Loss Angle	$T_{g\delta}$	Para. 9.4.1.2	-	15	10-4
3	Insulation Resistance	R_{i}	Para. 9.4.1.3	100(1)	-	GΩ
4	Voltage Proof	VP	Para. 9.4.1.4	2.5U _R	-	Vdc

NOTES

1. For C equal to, or less than 10 000pF.

For C greater than 10 000pF, R_i×C=1 000 seconds minimum.

TABLE 3 - ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES

No.	CHARACTERISTICS	SYMBOL	ESCC 3009	LIMITS		UNIT	REMARKS	
INO.	CHARACTERISTICS	TEST CONDITIONS		MIN.	MAX.	ONT	REMARKO	
3	Insulation Resistance at +125±3°C	R _i	Para. 9.4.1.3	10 000	-	МΩ	Notes 1, 2 and 4	
5(i)	Temperature Coefficient	TC	Para. 9.11 Between -55 and +20 ±2°C Between +20 ±2 and +125°C	-30 -30	+30 +30	10 ⁻⁶ /°C	5 parts for each capacitance value. Notes 2 and 5	
5(ii)	Temperature Coefficient	TC	Para. 9.11 Between +20 ±2 and +125°C	-30	+30	10 ⁻⁶ /°C	5 parts for each dielectric lot. Notes 3 and 5	

NOTES

- 1. Single sample; Inspection Level S3; AQL = 2.5%.
- 2. Applicable to Level 'B' only.
- 3. Applicable to Level 'C' only.
- 4. For C equal to, or less than 10 000pF.

For C greater than 10 000pF, $R_i \times C = 100$ seconds minimum.

- 5. If 1 failure out of 5 parts, then test 100%.
 - 1.0% rejects maximum allowed in case of 100% testing.



PAGE 13

ISSUE 4

TABLE 4 - PARAMETER DRIFT VALUES

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
1	Capacitance Change	<u>ΔC</u> C	ESCC Gen. Spec. 3009	Para's. 9.4.2 and 9.4.1.1	± 1.0	%

FIGURE 4 - TEST CIRCUITS

Not applicable.

TABLE 5 - CONDITIONS FOR BURN-IN AND OPERATING LIFE TESTS

No.	CHARACTERISTIC	SYMBOL	CONDITION	UNIT	
1	Ambient Temperature	T _{amb}	+ 125	°C	
2	Test Voltage	V _T	2.0U _R	٧	

FIGURE 5 - ELECTRICAL CIRCUIT FOR BURN-IN AND OPERATING LIFE TESTS

Not applicable.



PAGE 14

ISSUE 4

4.8 <u>ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESCC GENERIC SPECIFICATION No. 3009)</u>

4.8.1 Measurements and Inspections on Completion of Environmental Tests

The parameters to be measured and inspections to be performed on completion of environmental tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at T_{amb} = +22 ±3 °C.

4.8.2 Measurements and Inspections at Intermediate Points during Endurance Tests

The parameters to be measured and inspections to be performed at intermediate points during endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.8.3 Measurements and Inspections on Completion of Endurance Tests

The parameters to be measured and inspections to be performed on completion of endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.8.4 Conditions for Operating Life Test (Part of Endurance Testing)

The requirements for operating life testing are specified in Section 9 of ESCC Generic Specification No. 3009. The conditions for operating life testing shall be as specified in Table 5 for the Burn-in test.

4.8.5 <u>Electrical Circuit for Operating Life Test (Figure 5)</u>

Not applicable.



PAGE 15

ISSUE 4

TABLE 6 - MEASUREMENTS AND INSPECTIONS ON COMPLETION OF ENVIRONMENTAL TESTS AND AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING

	ESCC GENERIC SPECIFICATION NO. 3009		MEASUREMENTS AND INSPECTIONS		CVAADOL	LIMITS		UNIT
NO.	ENVIRONMENTAL AND ENDURANCE TESTS (1)	TEST METHOD AND CONDITIONS	IDENTIFICATION	CONDITIONS	SYMBOL	MIN.	MAX.	0.411
01	Mounting	Para. 9.15	Final Examination Terminals	Good Tinning	-	-	-	-
			Final Measurements Capacitance	Table 2 Item 1	С	Record	Values	pF
			Tangent of Loss Angle	Table 2 Item 2	$T_{g\delta}$	Table 2	Item 2	10-4
			Insulation Resistance	Table 2 Item 3	R _i	Table 2	Item 3	$\mathbf{G}\Omega$
02	Adhesion	Para. 9.5	Final Examination					
			Visual Examination	Damage or loosening		- Table 2	ltom 1	-
			Capacitance	Table 2 Item 1	С	Table 2	nem i	pF
03	Solderability	Para. 9.6	Final Examination Visual Examination	Para. 9.6		-		
04	Rapid Change of	Para. 9.7	Initial Measurements	Para. 9.6	-			
	Temperature		Capacitance	Table 2 Item 1	С	item 01	Value	pF
l			Final Measurements	Recovery period				
				24 ± 2 hours	1			
			Visual Examination	No damage Table 2 Item 1	<u>ΔC</u>	-1.0	+1.0	%
			Capacitance Change	Table 2 Item 1	C	1.0	1 ' '	/~
			Tangent of Loss Angle	Table 2 Item 2	$T_{g\delta}$	-	(2)	10 -4
05	Climatic Test Sequence	Para. 9.8	Initial Measurements					
			Capacitance	Table 2 Item 1	С	Item 0	1 Value	рF
l			Final Measurements	Recovery Period	l			
				1 -24 hrs	1	1		
1			Visual Inspection	Para. 9.8.7		-	+2.0	- %
			Capacitance Change	Table 2 Item 1	<u>ΔC</u> C	-2.0	+2.0	70
		1	Tangent of Loss Angle	Table 2 Item 2	$T_{g\delta}$	1 -	(2)	10-4
			Insulation Resistance	Table 2 Item 3	R _i	10 (3)	-	GΩ
06	Damp Heat	Para. 9.9	Initial Measurements			1	I	
1	Steady State		Capacitance	Table 2 Item 1	С	Item 0	1 Value	pF
	4.3		Final Measurements	Recovery Period 6 - 24 hrs				
	1		Visual Examination	No damage	l .	1 .	1 .	_
		1	Capacitance Change	Table 2 Item 1	<u>ΔC</u>	-2.0	+2.0	%
1			Capacitatice Charige	Table 2 Relit	<u>C</u>	0		1 ~
			Tangent of Loss Angle	Table 2 Item 2	$T_{g\delta}$		(2)	10 -4
1			Insulation Resistance	Table 2 Item 3	Ri	10 (3)	1 -	$\mathbf{G}\Omega$

NOTES

- 1. The tests in this table refer to either Chart IV or V and shall be used as applicable.
- 2. Twice the values specified in Table 2 of this specification.
- 3. For $C \le 10\ 000pF$;
 - For C > 10 000pF, $R_i \times C = 100$ seconds minimum.



PAGE 16

ISSUE 4

TABLE 6 - MEASUREMENTS AND INSPECTIONS ON COMPLETION OF ENVIRONMENTAL TESTS AND AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING (CONT'D)

NO.	ESCC GENERIC SPECIFICATION NO. 3009		MEASUREMENTS AND INSPECTIONS		SYMBOL	LIMITS		UNIT
NO.	ENVIRONMENTAL AND ENDURANCE TESTS (1)		IDENTIFICATION	CONDITIONS	STMBOL	MIN.	MAX.	
07	Operating Life		Initial Measurements Capacitance Intermediate Measurements to be performed at 1000 hrs (Chart IV)	Table 2 Item 1 Recovery period 1 hour min	С	Item 0	1 Value	pF
			Capacitance Change	Table 2 Item 1	<u>ΔC</u> . C	- 3.0	+3.0	%
			Insulation Resistance Final Measurements	Table 2 Item 3 Recovery period 24 ± 2 hours	R _i	10 (3)	-	G Ω
	The second secon	and the second s	Capacitance Change	Table 2 Item 1	<u>ΔC</u> C	-3.0	+3.0	%
			Tangent of Loss Angle Insulation Resistance Voltage Proof Visual Examination	Table 2 Item 2 Table 2 Item 3 Table 2 Item 4 No damage	T _{gδ} R _i VP -	- 10 (3) Table 2	(2) - 2 Item 4 -	10- ⁴ GΩ V -
08	Temperature Coefficient	Para. 9.11	Capacitance Changes	Table 3 Item 5(i) or 5(ii)	TC		ole 3 i) or 5(ii)	10 ⁻⁶ /°C

NOTES

- 1. The tests in this table refer to either Chart IV or V and shall be used as applicable.
- 2. Twice the values specified in Table 2 of this specification.
- 3. For $C \le 10\ 000pF$;

For C > 10 000pF, $R_i \times C = 100$ seconds minimum.



PAGE 17

ISSUE 4

APPENDIX 'A'

Page 1 of 1

AGREED DEVIATIONS FOR VITRAMON LTD. (UK)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS					
Para. 4.2.1	Microsectioning may be performed using Vitramon document QCN-020 (Issue as per P.I.D.).					



PAGE 18

ISSUE 4

APPENDIX 'B'

Page 1 of 1

AGREED DEVIATIONS FOR AVX (F)

ITEMS AFFECTED		DESCRIPTION OF DEVIATIONS
	data provided by	Para. 9.4.3, Table 3 Temperature Coefficient may be replaced with the ceramic material supplier, using AVX/TPC documents (issue as 20BCR**AQ - 1A-220022DCR**AQ.