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# CAPACITORS, FIXED, CHIPS, CERAMIC DIELECTRIC, TYPE II, BASED ON TYPE 1210 ESCC Detail Specification No. 3009/009

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# **DOCUMENTATION CHANGE NOTICE**

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DCR No.	CHANGE DESCRIPTION					
608	Specification upissued to incorporate technical and editorial changes per DCRs.					
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#### 1. GENERAL

#### 1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics, test and inspection data for Capacitors, Fixed, Chips, Ceramic Dielectric, Type II, based on Type 1210. It shall be read in conjunction with ESCC Generic Specification No. 3009, the requirements of which are supplemented herein.

#### 1.2 RANGE OF COMPONENTS

The range of capacitors covered by this specification is given in Table 1(a).

#### 1.3 MAXIMUM RATINGS

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the capacitors specified herein, are as scheduled in Table 1(b).

#### 1.4 PARAMETER DERATING INFORMATION (FIGURE 1)

Not applicable.

#### 1.5 PHYSICAL DIMENSIONS

The physical dimensions of the capacitors specified herein are shown in Figure 2.

#### 1.6 <u>FUNCTIONAL DIAGRAM</u>

The functional diagram for the capacitors specified herein is shown in Figure 3.



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# TABLE 1(a) - RANGE OF COMPONENTS

CAPACITANCE RANGE (pF)	TOLERANCE (±) (%)	VALUES SERIES	RATED VOLTAGE (U <sub>R</sub> ) (V)
680 to 3900	5.0	E 24	400
680 to 3900	10	E 12	400
680 to 4700	20	E 6	400
680 to 18000	5.0	E 24	200
680 to 18000	10	E 12	200
680 to 22000	20	E 6	200
1000 to 220000	5.0	E 24	100
1000 to 220000	10	E 12	100
1000 to 220000	20	E 6	100
15000 to 390000	5.0	E 24	50
15000 to 390000	10	E 12	50
15000 to 330000	20	E 6	50
15000 to 560000	5.0	E 24	25
15000 to 560000	10	E 12	25
15000 to 470000	20	E 6	25
33000 to 820000	5.0	E 24	16
33000 to 820000	10	E 12	16
33000 to 680000	20	E 6	16

# **NOTES**

1. 7 variants are specified in Para. 4.4.1, Figure 2 and Table 3.



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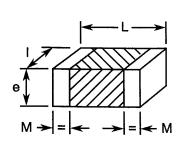
#### TABLE 1(b) - MAXIMUM RATINGS

No.	CHARACTERISTICS	SYMBOL	LIMITS		UNIT	DEMARKS	
			MIN.	MAX.	ONLI	REMARKS	
1	Rated Voltage	U <sub>R</sub>	See Table 1(a)		V	-	
2	Operating Temperature Range	T <sub>amb</sub>	<b>– 55</b>	+ 125	°C	Without derating	
3	Storage Temperature Range	T <sub>stg</sub>	-55	+ 125	°C	•	
4	Maximum Soldering Temperature	T <sub>sol</sub>	-	+ 260	°C	Soldering time: t: <10 sec.	

# **FIGURE 1 - PARAMETER DERATING INFORMATION**

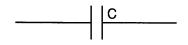
Not applicable.

#### FIGURE 2 - PHYSICAL DIMENSIONS



			DIMENSI	ONS (mm)		
SYMBOL		S 01, 03, 6	3, VARIANTS 02, 04, VARIA 05		NT 07	
	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.
L	2.8	3.6	2.8	4.1	2.8	3.6
I	2.2	2.8	2.2	3.3	2.2	2.8
е	-	2.3	•	2.3	-	1.8
M	0.2	0.75	0.2	0.75	0.2	0.75

# **FIGURE 3 - FUNCTIONAL DIAGRAM**





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#### 2. APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:-

(a) ESCC Generic Specification No. 3009 for Capacitors, Fixed, Chips, Ceramic Dielectric, Types I and II.

#### 3. TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply. In addition, the following symbols are used:-

TCC = Temperature Characteristic.

 $V_T$  = Test Voltage.

#### 4. REQUIREMENTS

#### 4.1 GENERAL

The complete requirements for procurement of the capacitors specified herein shall be as stated in this specification and ESCC Generic Specification No. 3009 for Capacitors, Fixed, Chips, Ceramic Dielectric, Types I and II. Deviations from the Generic Specification, applicable to this specification only, are listed in Para. 4.2.

Deviations from the applicable Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

#### 4.2 <u>DEVIATIONS FROM GENERIC SPECIFICATION</u>

#### 4.2.1 <u>Deviations from Special In-process Controls</u>

None.

#### 4.2.2 Deviations from Final Production Tests (Chart II)

None.

#### 4.2.3 Deviations from Burn-in and Electrical Measurements (Chart III)

None.

#### 4.2.4 <u>Deviations from Qualification Tests (Chart IV)</u>

None.

#### 4.2.5 <u>Deviations from Lot Acceptance Tests (Chart V)</u>

None.

#### 4.3 <u>MECHANICAL REQUIREMENTS</u>

#### 4.3.1 <u>Dimension Check</u>

The dimensions of the capacitors specified herein shall be verified in accordance with the requirements set out in Para. 9.3 of ESCC Generic Specification No. 3009 and shall conform to those shown in Figure 2 of this specification.



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#### 4.3.2 Weight

The maximum weight of the capacitors specified herein shall be 0.15 grammes.

#### 4.3.3 Adhesion

The requirements for adhesion are specified in Para. 9.5 of ESCC Generic Specification No. 3009.

#### 4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the capacitors specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material does not guarantee acceptance of the finished product.

#### 4.4.1 <u>Terminations</u>

- Variant 01: The capacitors shall be terminated with AgPd pads.
- Variant 02: The capacitors shall be terminated with AgPd with solder coating, 62 Sn, 36 Pb, 2.0 Ag %, +188°C.
- Variant 03: The capacitors shall be terminated with AgPdPt pads.
- Variant 04: The capacitors shall be terminated with AgPdPt with solder coating, 62 Sn, 36 Pb, 2.0 Ag %, +188 °C.
- Variant 05: The capacitors shall be terminated with Ag, Ni barrier with solder coating, 62 Sn, 36Pb, 2.0 Ag %, +188°C.
- Variants 06 and 07: The capacitors shall be terminated with Ag, Ni barrier with coating tinlead, near eutectic, minimum 10% lead.

All the above Variants are suitable for reflow soldering.

#### N.B.

Variants 06 and 07 have the preferred termination finish for the specified chip size (see Figure 2).

#### 4.5 MARKING

#### 4.5.1 General

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and the following paragraphs.

These components being too small to accommodate the marking as specified hereafter, the marking information in full shall accompany each component in its primary package. Such marking shall comprise:-

- (a) The ESCC Component Number.
- (b) Characteristics and Ratings.
- (c) Traceability Information.

#### 4.5.2 The ESCC Component Number

Each component shall bear the ESCC Component Number which shall be constituted and marked as follows:

	300900901
Detail Specification Number	
Type Variant (see Para. 4.4.1 and Figure 2 and Table 3)	
Testing Level (B or C, as applicable)	



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#### 4.5.3 <u>Electrical Characteristics and Ratings</u>

The electrical characteristics and ratings to be marked in the following order of precedence are:-

- (a) Capacitance Value.
- (b) Tolerance.
- (c) Rated Voltage.

The information shall be constituted and marked as follows:-

	<u>102</u> KE 1 1 1
Capacitance Value (1000pF)	
Tolerance (±10%)	
Rated Voltage (100V)	

#### 4.5.3.1 Capacitance Values

The capacitance values shall be expressed by means of the following codes. The unit quantity for marking shall be picofarads (pF).

CAPACITANCE VALUE	CODE
XX10 <sup>1</sup>	XX1
XX10 <sup>2</sup>	XX2
XX10 <sup>3</sup>	XX3
XX10⁴	XX4

#### 4.5.3.2 Tolerances

The tolerances on capacitance values shall be indicated by the code letters specified hereafter.

TOLERANCE (%)	CODE LETTER
± 5.0	J
± 10	K
±20	M

#### 4.5.3.3 Rated Voltage

The rated voltages shall be indicated by the code letters specified hereafter.

RATED VOLTAGE (U <sub>R</sub> ) (V)	CODE LETTER
16	Х
25	Α
50	С
100	E
200	G
400	K



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#### 4.5.4 Traceability Information

Traceability information shall be marked in accordance with the requirements of ESCC Basic Specification No. 21700.

- (a) Manufacturing Date Code.
- (b) Manufacturer's Name.

#### 4.6 <u>ELECTRICAL MEASUREMENTS</u>

#### 4.6.1 Electrical Measurements at Room Temperature

The parameters to be measured at room temperature are scheduled in Table 2. Unless otherwise specified, the measurements shall be performed at  $T_{amb} = +22 \pm 3$  °C.

#### 4.6.2 Electrical Measurements at High and Low Temperatures

The parameters to be measured at high and low temperatures are scheduled in Table 3.

#### 4.6.3 Circuits for Electrical Measurements

A circuit for use in performing the electrical measurements listed in Table 2 of this specification is shown in ESCC Generic Specification No. 3009.

#### 4.7 BURN-IN TESTS

#### 4.7.1 Parameter Drift Values

The parameter drift values applicable to burn-in are specified in Table 4 of this specification. Unless otherwise stated, measurements shall be performed at  $T_{amb}$  = +22±3 °C. The parameter drift values ( $\Delta$ ) applicable to the scheduled parameters shall not be exceeded. In addition to these drift value requirements for a given parameter, the appropriate limit value specified in Table 2 shall not be exceeded.

#### 4.7.2 Conditions for Burn-in

The requirements for burn-in are specified in Section 7 of ESCC Generic Specification No. 3009. The conditions for burn-in shall be as specified in Table 5 of this specification.

On completion of burn-in, a recovery period of  $24 \pm 2$  hours is necessary before performance of the end-measurements.

#### 4.7.3 Electrical Circuit for Burn-in (Figure 5)

Not applicable.



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#### TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE

No.	CHARACTERISTICS SYME	SYMBOL	ESCC 3009 TEST CONDITIONS	LIMITS		UNIT
INO.	OHANACTENISTICS	STIMBOL		MIN.	MAX.	ONT
1	Capacitance	С	Para. 9.4.1.1	-5.0 -10 -20	+5.0 +10 +20	% (1)
2	Tangent of Loss Angle	$T_{g\delta}$	Para. 9.4.1.2	1	250	10 <sup>-4</sup>
3	Insulation Resistance	R <sub>i</sub>	Para. 9.4.1.3	100 (2)	-	GΩ
4	Voltage Proof	VP	Para. 9.4.1.4	2.5U <sub>R</sub>	-	٧

#### **NOTES**

- 1. The capacitance limits before burn-in are -5.0 and +20%.
- 2. For C equal to, or less than, 10 000pF.

For C greater than 10 000pF,  $R_i \times C = 1$  000 seconds minimum.

#### TABLE 3 - ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES

No.	CHARACTERISTICS	SYMBOL	ESCC 3009	LIMITS		UNIT	REMARKS	
L.	OTATIAOTERIOTOS	STIVIDOL	TEST CONDITIONS	MIN.	MAX.	ONIT	RLIVIARRO	
3	Insulation Resistance at +125±3°C	R <sub>i</sub>	Para. 9.4.1.3	10 000	-	МΩ	Notes 1, 2 and 4	
5(i)	Temperature Characteristic $\frac{\Delta C}{C} = f(T)$	TCC	Para. 9.12 $V_T = 0$ (for Variants 01 to 07) $V_T = U_R$ (for Variants 01 to 06)	-20 -30	+ 20 + 20	%	5 parts for each capacitance value. Notes 2, 5 and 6	
5(ii)	Temperature Characteristic $\Delta C = f(T)$	TCC	Para. 9.12 $V_T = 0$ (for Variants 01 to 07) $V_T = U_R$ (for Variants 01 to 06)	-20 -30	+ 20 + 20	%	5 parts for each dielectric lot. Notes 3,5 and 6	

#### **NOTES**

- 1. Single sample; Inspection Level S3; AQL = 2.5%.
- 2. Applicable to Level 'B' only.
- 3. Applicable to Level 'C' only.
- 4. For C equal to, or less than, 10 000pF.

For C greater than 10 000pF,  $R_i \times C = 100$  seconds minimum.

- 5. If 1 failure out of 5 parts, then test 100%.
  - 1.0% rejects maximum allowed in case of 100% testing.
- 6. Variant 07 is X7R dielectric. Delta C/C at Ur is typically -60% for this dielectric type.



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#### TABLE 4 - PARAMETER DRIFT VALUES

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
1	Capacitance Change	<u>ΔC</u> C	ESCC Gen. Spec. 3009	Para's. 9.4.2 and 9.4.1.1	± 15	%

#### FIGURE 4 - TEST CIRCUITS

Not applicable.

### TABLE 5 - CONDITIONS FOR BURN-IN AND OPERATING LIFE TESTS

No.	CHARACTERISTIC	SYMBOL	CONDITION	UNIT	
1	Ambient Temperature	T <sub>amb</sub>	+ 125	۰C	
2	Test Voltage	V <sub>T</sub>	2.0U <sub>R</sub>	V	

# FIGURE 5 - ELECTRICAL CIRCUIT FOR BURN-IN AND OPERATING LIFE TESTS

Not applicable.



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# 4.8 <u>ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESCC GENERIC SPECIFICATION No. 3009)</u>

#### 4.8.1 <u>Measurements and Inspections on Completion of Environmental Tests</u>

The parameters to be measured and inspections to be performed on completion of environmental tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb} = +22 \pm 3$ °C.

#### 4.8.2 Measurements and Inspections at Intermediate Points during Endurance Tests

The parameters to be measured and inspections to be performed at intermediate points during endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb} = +22 \pm 3$ °C.

#### 4.8.3 <u>Measurements and Inspections on Completion of Endurance Tests</u>

The parameters to be measured and inspections to be performed on completion of endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb} = +22 \pm 3$  °C.

#### 4.8.4 Conditions for Operating Life Test (Part of Endurance Testing)

The requirements for operating life testing are specified in Section 9 of ESCC Generic Specification No. 3009. The conditions for operating life testing shall be as specified in Table 5 for the Burn-in test.

#### 4.8.5 <u>Electrical Circuit for Operating Life Test (Figure 5)</u>

Not applicable.



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# TABLE 6 - MEASUREMENTS AND INSPECTIONS ON COMPLETION OF ENVIRONMENTAL TESTS AND AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING

110	ESCC GENERIC SPECIFICATION NO. 3009		MEASUREMENTS AND INSPECTIONS		SYMBOL	LIMITS		UNIT
NO.	ENVIRONMENTAL AND ENDURANCE TESTS (1)	TEST METHOD AND CONDITIONS	IDENTIFICATION CONDITIONS		3 T MIBOL	MIN.	MAX.	OMI
01	Mounting	Para. 9.15	Final Examination Terminals Final Measurements	Good Tinning	-	-	-	
		·	Capacitance	Table 2 Item 1	С	Record	Values	рF
			Tangent of Loss Angle	Table 2 Item 2	$T_{g\delta}$	Table 2	Item 2	10-4
			Insulation Resistance	Table 2 Item 3	Ř	Table 2	Item 3	$\mathbf{G}\Omega$
02	Adhesion	Para. 9.5	Final Examination					
			Visual Examination	Damage or loosening				-
			Capacitance	Table 2 Item 1	С	Table 2	Item 1	pF
03	Solderability	Para. 9.6	Final Examination	_				
	, the second of		Visual Examination	Para. 9.6			-	•
04	Rapid Change of Temperature	Para. 9.7	Initial Measurements Capacitance	Table 2 Item 1	С	Item 01	Value	pF
Į.			Final Measurements	Recovery period 24 ± 2 hours				
			Visual Examination	No damage	-	-	-	
			Capacitance Change	Table 2 Item 1	<u>ΔC</u>	-10	+10	%
			Tangent of Loss Angle	Table 2 Item 2	C T <sub>gδ</sub>	-	(2)	10 <del>-4</del>
05	Climatic Test Sequence	Para. 9.8	Initial Measurements					
			Capacitance	Table 2 Item 1	С	Item 0	Value	pF
			Final Measurements	Recovery Period 1 -24 hrs		İ		
ı			Visual Inspection	Para. 9.8.7	_	1 .		_
			Capacitance Change	Table 2 Item 1	ΔC	-10	+10	%
					C			
1			Tangent of Loss Angle	Table 2 Item 2	$T_{g\delta}$	-	(2)	10-4
			Insulation Resistance	Table 2 Item 3	R <sub>i</sub>	3.0(3)	Ŀ	GΩ
06	Damp Heat	Para. 9.9	Initial Measurements					
1	Steady State		Capacitance	Table 2 Item 1	С	item 0	1 Value	pF
			Final Measurements	Recovery Period 6 - 24 hrs				
1			Visual Examination	No damage		1 -	_	
			Capacitance Change	Table 2 Item 1	<u>ΔC</u>	-10	+10	%
Ì			Tangent of Loss Angle	Table 2 Item 2	C T <sub>gδ</sub>	_	(2)	10-4
			Insulation Resistance	Table 2 Item 3	'gô R <sub>i</sub>	3.0(3)	-	GΩ

#### NOTES

- 1. The tests in this table refer to either Chart IV or V and shall be used as applicable.
- 2. Twice the values specified in Table 2 of this specification.
- 3. Or 30 seconds for C > 10 000pF.
- 4. Or 100 seconds for C > 10 000pF.



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# TABLE 6 - MEASUREMENTS AND INSPECTIONS ON COMPLETION OF ENVIRONMENTAL TESTS AND AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING (CONT'D)

NO.	ESCC GENERIC SPECIFICATION NO. 3009		MEASUREMENTS AND	0.4.50	LIMITS				
NO.	ENVIRONMENTAL AND ENDURANCE TESTS (1)		IDENTIFICATION CONDITIONS		SYMBOL	MIN.	MAX.	UNIT	
07	Operating Life	Para. 9.10	Initial Measurements Capacitance Intermediate Measurements to be performed at 1000 hrs (Chart IV)	Table 2 Item 1 Recovery period 24 ± 2 hours	С	Item 0	1 Value	pF	
			Capacitance Change	Table 2 Item 1	<u>ΔC</u> C	15	+ 15	%	
			Insulation Resistance Final Measurements	Table 2 Item 3 Recovery period 24 ± 2 hours	R <sub>i</sub>	10 (4)	-	GΩ	
	1 ( <del>1 (1 )</del>		Capacitance Change	Table 2 Item 1	<u>ΔC</u> C	-15	+ 15	%	
			Tangent of Loss Angle Insulation Resistance Voltage Proof Visual Examination	Table 2 Item 2 Table 2 Item 3 Table 2 Item 4 No damage	T <sub>gð</sub> R <sub>i</sub> VP -	- 10 (4) Table 2 -	(2) - 2 Item 4 -	10 <sup>-4</sup> GΩ V -	
80	Temperature Characteristic	Para. 9.12	Capacitance Changes	Table 3 Item 5(i) or 5(ii)	TCC		ole 3 ) or 5(ii)	%	

#### **NOTES**

- 1. The tests in this table refer to either Chart IV or V and shall be used as applicable.
- 2. Twice the values specified in Table 2 of this specification.
- 3. Or 30 seconds for C > 10 000pF.
- 4. Or 100 seconds for C > 10 000pF.



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# APPENDIX 'A'

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# AGREED DEVIATIONS FOR VITRAMON LTD. (UK)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
	Microsectioning may be performed using Vitramon document QCN-020 (Issue as per P.I.D.).