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# DIODES, VOLTAGE REGULATOR, BASED ON BZX55 SERIES ESCC Detail Specification No. 5102/010

## ISSUE 1 October 2002





#### **ESCC Detail Specification**

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## DIODES, VOLTAGE REGULATOR, BASED ON BZX55 SERIES

ESA/SCC Detail Specification No. 5102/010



## space components coordination group

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#### **DOCUMENTATION CHANGE NOTICE**

Rev. Letter	Rev. Date	CHANGE Reference Item	Approved DCR No.
		This issue supersedes Issue 3 and incorporates all modifications agreed on the basis of Policy DCR's 21022/21025 and the following DCR's:-	
		Para. 2 : MIL-STD-202 added Table 1(a) : Type Variants for lead length added. Note added Table 1(b) : Amended Figure 1 : Amended Figure 2 : Inches and Lead Variants added  Para. 4.2.3 : X-Ray test deleted  Tables 2(d.c.) (a.c.) : Format changed Table 2(a.c.) : Sample note added Tables 3 and 4 : Format changed Table 4 : V <sub>Z</sub> limit changed to ±5% Table 4 : I <sub>R</sub> limits changed Table 6 : Format changed Appendix A : New lead material added Appendix A : Qual. and LAT testing changed Appendix B : Amended	23177 22206 23177 23177 23177/ 22206 22206/ 22236 23177 22206 23177 22206 23177 22206 23177 22206 24042 24029/
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#### 1. **GENERAL**

#### 1.1 SCOPE

This specification details the ratings, physical and electrical characteristics, test and inspection data for Diodes, Voltage Regulator, based on the BZX55 Series.

It shall be read in conjunction with ESA/SCC Generic Specification No. 5000, the requirements of which are supplemented herein.

#### 1.2 COMPONENT TYPE VARIANTS

Variants of the basic diodes specified herein, which are also covered by this specification, are given in Table 1(a).

#### 1.3 MAXIMUM RATINGS

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the diodes specified herein, are scheduled in Table 1(b).

#### 1.4 PARAMETER DERATING INFORMATION

The derating information applicable to the diodes specified herein is shown in Figure 1.

#### 1.5 PHYSICAL DIMENSIONS

The physical dimensions of the diodes specified herein are shown in Figure 2.

#### 1.6 FUNCTIONAL DIAGRAM

The functional diagram, showing lead identification, of the diodes specified herein, is shown in Figure 3.

#### 1.7 HIGH TEMPERATURE TEST PRECAUTIONS

For tin-lead plated or solder-dipped lead finish, all tests to be performed at a temperature that exceeds +125°C shall be carried out in a 100% inert atmosphere.

#### 2. <u>APPLICABLE DOCUMENTS</u>

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESA/SCC Generic Specification No. 5000 for Discrete Semiconductors.
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices.
- (c) MIL-STD-202, Test Methods for Electronic and Electrical Component Parts.

#### 3. TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESA/SCC Basic Specification No. 21300 shall apply.

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TABLE 1(a) - TYPE VARIANTS

and		4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	<u>ب</u>	4
Lead Material a	(15)	C3 or C4	C3 or C4	C3 or C4	C3 or C4	C3 or C4	C3 or C4	C3 or C4	C3 or C4	C3 or C4														
l <sub>R</sub> max at Tamb = + 150°C (,,,∆)	(14)	100	20	40	40	40	40	20	10	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
lzk (mA)	(13)	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
Z <sub>K</sub> (max.	(12)	009	009	900	900	009	009	009	009	220	450	200	150	20	20	20	20	20	90	110	110	170	170	220
T <sub>CVZ</sub> (%/°C) (11)	Мах.	- 0.06	- 0.06	90.0 -	- 0.05	- 0.04	- 0.03	- 0.01	+ 0.01	+ 0.05	+ 0.06	+ 0.07	+ 0.08	+ 0.09	+ 0.09	+ 0.10	+0.11	+ 0.11	+0.11	+0.11	+0.11	+ 0.11	+0.11	+0.11
T <sub>CVZ</sub> (1	Min.	- 0.08	- 0.08	- 0.08	- 0.08	- 0.08	- 0.07	- 0.04	- 0.03	- 0.02	- 0.01	0	+ 0.01	+ 0.01	+ 0.01	+ 0.02	+ 0.03	+ 0.03	+ 0.03	+ 0.03	+ 0.03	+ 0.03	+ 0.03	+ 0.03
l <sub>R</sub> (max. µA)	(10)	50	10	4.0	2.0	2.0	2.0	1.0	0.5	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1
V <sub>R</sub> (S)	(6)	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	2.0	3.0	5.0	6.2	6.8	7.5	8.2	9.1	10	=	12	13	15
Z <sub>Zmax</sub> (Ω)	(8)	85	82	85	85	85	85	75	09	35	25	10	8.0	7.0	7.0	10	15	20	20	56	30	40	20	55
I <sub>Zmax</sub> (mA)	(2)	115	135	125	115	105	92	90	82	80	20	64	28	53	47	43	40	36	32	53	27	24	2	50
lz (mA)	(9)	5.0	5.0	5.0	5.0	5.0	5.0	5.0	2.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	2.0	2.0	2.0	2.0	5.0	2.0	2.0	5.0
Vz Max.	(5)	2.56	2.9	3.2	3.5	3.8	4.1	4.6	2.0	5.4	0.9	9.9	7.2	7.9	8.7	9.6	10.6	11.6	12.7	14.1	15.6	17.1	19.1	21.2
Vz Min.	(4)	2.28	2.5	2.8	3.1	3.4	3.7	4.0	4.4	4.8	5.2	5.8	6.4	7.0	7.7	8.5	9.4	10.4	11.4	12.4	13.8	15.3	16.8	18.8
Vz Nom.	(3)	2.4	2.7	3.0	3.3	3.6	3.9	4.3	4.7	5.1	5.6	6.2	8.9	7.5	8.2	9.1	10	-	12	13	15	16	18	20
Based on Type		55 - C2V4	55 - C2V7	55 - C3V0	55 - C3V3	55 - C3V6	55 - C3V9	55 - C4V3	55 - C4V7	55 - C5V1	55 - C5V6	55 - C6V2	55 - C6V8	55 - C7V5	55 - C8V2	55 - C9V1	55 - C10	55 - C11	55 - C12	55 - C13	55 - C15	55 - C16	55 - C18	55 - C20
Variant (See	(1)	01, 48	02, 49	03, 50	04, 51	05, 52	06, 53	07, 54	08, 55	09, 56	10, 57	11, 58	12, 59	13, 60	14, 61	15, 62	16, 63	17, 64	18, 65	19, 66	20, 67	21, 68	22, 69	23, 70

NOTES: See Page 7.

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TABLE 1(a) - TYPE VARIANTS (Cont'd)

Variants 01 to 47 have lead lengths of 1.0inch (25.4mm) min.
 Variants 48 to 94 have lead lengths of 0.5 inch (12.7m.) min.
 See Figure 2.



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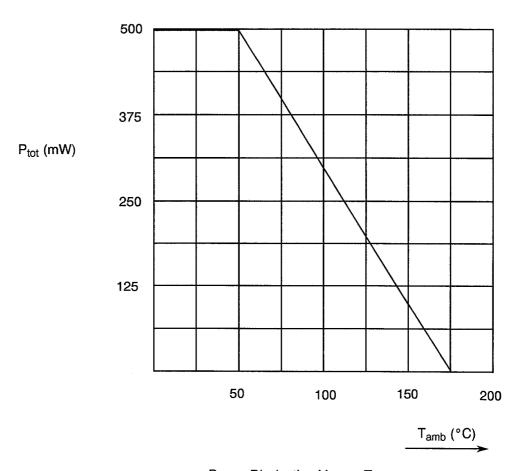
#### TABLE 1(b) - MAXIMUM RATINGS

No.	CHARACTERISTICS	SYMBOL	MAXIMUM RATINGS	UNIT	REMARKS
1	Power Dissipation	P <sub>tot</sub>	500	mW	T <sub>amb</sub> ≤ +25°C See Notes 1, 2
2	Operating Temperature Range	T <sub>op</sub>	-55 to +150	°C	T <sub>amb</sub>
3	Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C	
4	Soldering Temperature	T <sub>sol</sub>	+ 260	°C	Time: ≤10 seconds; Distance from case: ≥ 1.5mm

#### **NOTES**

- 1. Leads shall be maintained at ambient temperature 4 mm from the body.
- 2. For derating at  $T_{amb}$  > +25°C see Figure 1.

#### **FIGURE 1 - PARAMETER DERATING INFORMATION**



Power Dissipation Versus Temperature

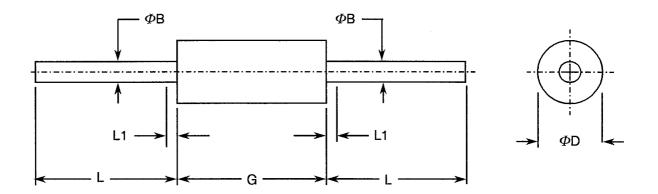


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#### FIGURE 2 - PHYSICAL DIMENSIONS

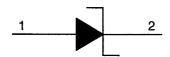


SYMBOL	MILLIM	ETRES	INC	HES	NOTES		
STIVIBUL	MIN. (3	) MAX.	MIN.	MAX.	NOTES		
$\Phi$ B	0.458	0.558	0.018	0.022			
$\Phi$ D	1.42	1.91	0.056	0.075	1		
G	3.56	4.57	0.140	0.180	1		
L	Note 4	•	Note 4	-	-		
L1	-	2.5		0.098	2		

#### NOTES

- 1. Package contour optional within cylinder of diameter  $\Phi D$  and length G. Slugs, if any, shall be included within this cylinder but shall not be subject to the minimum limit of  $\Phi D$ .
- 2. Lead diameter not controlled in this zone to allow for flash, lead finish build-up, and minor irregularities other than slugs.
- 3. The millimetre dimensions are derived from the original inch dimensions.
- 4. Variants 01 to 47 have leads with L of 1 inch (25.4 mm) min and variants 48 to 94 have leads with L of 0.5 inch (12.7 mm) min.

#### FIGURE 3 - FUNCTIONAL DIAGRAM



- 1. Anode
- 2. Cathode

#### **NOTES**

1. The cathode end shall be marked with a coloured ring.



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#### 4. **REQUIREMENTS**

#### 4.1 GENERAL

The complete requirements for procurement of the diodes specified herein are stated in this specification and ESA/SCC Generic Specification No. 5000 for Discrete Semiconductors. Deviations from the Generic Specification applicable to this specification only, are listed in Para. 4.2.

Deviations from the applicable Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESA/SCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

#### 4.2 DEVIATIONS FROM GENERIC SPECIFICATION

#### 4.2.1 Deviations from Special In-process Controls

None.

#### 4.2.2 <u>Deviations from Final Production Tests (Chart II)</u>

- (a) Bond Strength Test: Shall not be performed.
- (b) Die Shear Test: Shall not be performed.
- (c) Particle Impact Noise Detection (PIND) Test: Not applicable.
- (d) Thermal Shock Test: Shall be performed in accordance with Test Method 107, Condition 'B', of MIL-STD-202.

#### 4.2.3 <u>Deviations from Burn-in and Electrical Measurements (Chart III)</u>

- (a) H.T.R.B. Test: Shall not be performed.
- (b) Radiographic Inspection: Not applicable.

#### 4.2.4 Deviations from Qualification Tests (Chart IV)

- (a) Bond Strength Test: Shall not be performed.
- (b) Die Shear Test: Shall not be performed.

#### 4.2.5 <u>Deviations from Lot Acceptance Tests (Chart V)</u>

None.



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#### 4.3 MECHANICAL REQUIREMENTS

#### 4.3.1 Dimension Check

The dimensions of the diodes specified herein shall be checked. They shall conform to those shown in Figure 2.

#### 4.3.2 Weight

The maximum weight of the diodes specified herein shall be 0.2 grammes.

#### 4.3.4 Terminal Strength

The requirements for terminal strength testing are specified in Section 9 of ESA/SCC Generic Specification No. 5000. The test conditions shall be as follows:-

Test Condition:

′Α′.

Applied Force : Duration :

5.0 Newtons.10 seconds

#### 4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the diodes specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material does not guarantee acceptance of the finished product.

#### 4.4.1 <u>Case</u>

Glass, hermetically sealed.

#### 4.4.2 Lead Material and Finish

The requirements of the lead material shall be Type 'C' with either Type '3 or 4' finish in accordance with the requirements of ESA/SCC Basic Specification No. 23500.



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#### 4.5 MARKING

#### 4.5.1 General

The marking of all components delivered to this specification shall be in accordance with the requirements of ESA/SCC Basic Specification No. 21700. Each component shall be marked in respect of:-

- (a) Lead Identification.
- (b) The SCC Component Number.
- (c) Traceability Information.

#### 4.5.2 Lead Identification

Lead identification shall be as shown in Figures 2 and 3 of this specification.

#### 4.5.3 The SCC Component Number

Each component shall bear the SCC Component Number which shall be constituted and marked as follows:

Detail Specification Number	
Type Variant (see Table 1(a)) —	
Testing Level (B or C, as applicat	ole)

#### 4.5.4 <u>Traceability Information</u>

Each component shall be marked in respect of traceability information in accordance with the requirements of ESA/SCC Basic Specification No. 21700.

#### 4.5.5 Marking of Small Components

When it is considered that the component is too small to accommodate the marking as specified above, as much as space permits shall be marked. The order of precedence shall be as follows:-

- (a) Lead Identification.
- (b) The SCC Component Number.
- (c) Traceability Information.

The marking information in full shall accompany each component in its primary package.



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#### 4.6 ELECTRICAL MEASUREMENTS

#### 4.6.1 Electrical Measurements at Room Temperature

The parameters to be measured at room temperature are scheduled in Table 2. The measurements shall be performed at  $T_{amb} = +22 \pm 3$  °C.

#### 4.6.2 Electrical Measurements at High and Low Temperatures

The parameters to be measured at high and low temperatures are scheduled in Table 3.

#### 4.6.3 Circuits for Electrical Measurements

Circuits for use in performing the electrical measurements listed in Tables 2 and 3 of this specification are shown in Figure 4.

#### 4.7 BURN-IN TESTS

#### 4.7.1 Parameter Drift Values

The parameter drift values applicable to burn-in are specified in Table 4 of this specification. Unless otherwise stated, the measurements shall be performed at  $T_{amb}$  = +22±3 °C. The parameter drift value ( $\Delta$ ) applicable to the parameters scheduled, shall not be exceeded. In addition to these drift value requirements, the appropriate limit value specified in Table 2 for a given parameter shall not be exceeded.

#### 4.7.2 Conditions for Burn-in

The requirements for burn-in are specified in Section 7 of ESA/SCC Generic Specification No. 5000. The conditions for burn-in shall be as specified in Table 5.

#### 4.7.3 Electrical Circuits for Burn-in

Circuits for use in performing the burn-in tests are shown in Figure 5 of this specification.



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#### TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - d.c. PARAMETERS

No. C	CHARACTERISTICS	SYMBOL	MIL-STD-750	TEST CONDITION	LIMITS		UNIT
			TEST METHOD	TEST CONDITION	MIN.	MAX.	ONIT
1	Zener Voltage	V <sub>Z</sub>	4022	I <sub>Z</sub> = (1) mA	(2)	(3)	V
2	Reverse Current	l <sub>R</sub>	4016	V <sub>R</sub> = (4) V	-	(5)	μΑ

#### TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - a.c. PARAMETERS

No.	CHARACTERISTICS	SYMBOL	MIL-STD-750	TEST CONDITION	LIMITS		UNIT
110.	INU. TORANACTERISTICS STIVIBO		TEST METHOD	(Note 10)	MIN.	MAX.	
3	Small Signal Breakdown Impedance	Z <sub>Z</sub>	4051	I <sub>Z</sub> =(1) mA	-	(6)	Ω
4	Small Signal Breakdown Impedance	Z <sub>K</sub>	4051	I <sub>ZK</sub> = (8) mA	-	(7)	Ω

#### **NOTES**

- 1. See Table 1(a), Column 6.
- See Table 1(a), Column 4.
   See Table 1(a), Column 5.
- 4. See Table 1(a), Column 9.
- 5. See Table 1(a), Column 10.
- 6. See Table 1(a), Column 8.
- 7. See Table 1(a), Column 12.
- 8. See Table 1(a), Column 13.
- 9. See Table 1(a), Column 14.
- 10. Tests to be performed on a sample basis, LTPD 7 or less.

#### **FIGURE 4 - TEST CIRCUITS**

Not applicable.



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#### TABLE 3 - ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES

No.	CHARACTERISTICS	SVMBOL	MIL-STD-750	TEST CONDITIONS	LIMITS		UNIT
140.	OHAINOTENION	STWIDOL	TEST METHOD	See Note 10	MIN.	MAX.	ONIT
2	Reverse Current	I <sub>R</sub>	4016	T <sub>amb</sub> = + 150°C V <sub>R</sub> = (4)	-	(9)	μА

#### **NOTES**

1. See Page 14, Notes (1) to (9) only.

#### **TABLE 4 - PARAMETER DRIFT VALUES**

No.	CHARACTERISTICS	SYMBOL	SPEC. AND TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
1	Zener Voltage	V <sub>Z</sub>	As per Table 2	As per Table 2	± 5.0	%
2	Reverse Current for VZ>3V	l <sub>R</sub>	As per Table 2	As per Table 2	± 100 or 50 see Note	% (3) nA
2	Reverse Current for VZ≤3V	l <sub>R</sub>	As per Table 2	As per Table 2	+ 50 -100	% %

#### NOTES

1. Whichever is greater.

#### **TABLE 5 - CONDITIONS FOR BURN-IN**

No.	CHARACTERISTIC	SYMBOL	CONDITION	UNIT
1	Ambient Temperature	T <sub>amb</sub>	+25 Distance from case: 4mm	°C
2	Working Current Current	I <sub>Zmax</sub>	See Table 1(a), Column 7	mA

#### FIGURE 5 - ELECTRICAL CIRCUIT FOR BURN-IN

Not applicable.



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### 4.8 <u>ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESA/SCC GENERIC SPECIFICATION NO. 5000)</u>

#### 4.8.1 Electrical Measurements on Completion of Environmental Tests

The parameters to be measured on completion of environmental tests are scheduled in Table 2. The measurements shall be performed at  $T_{amb} = +22 \pm 3$  °C.

#### 4.8.2 <u>Electrical Measurements at Intermediate Points and on Completion of Endurance Tests</u>

The parameters to be measured at intermediate points and on completion of endurance testing are scheduled in Table 6.

#### 4.8.3 <u>Conditions for Operating Life Tests (Part of Endurance Testing)</u>

The requirements for operating life testing are specified in Section 9 of ESA/SCC Generic Specification No. 5000. The conditions for operating life testing shall be the same as specified in Table 5 for the burn-in test.

#### 4.8.4 Electrical Circuits for Operating Life Tests

The circuit to be used for performance of the operating life test shall be the same as shown in Figure 5 for burn-in.

#### 4.8.5 Conditions for High Temperature Storage Test (Part of Endurance Testing)

The requirements for the high temperature storage test are specified in ESA/SCC Generic Specification No. 5000. The temperature to be applied shall be the maximum storage temperature specified in Table 1(b) of this specification.



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### TABLE 6 - ELECTRICAL MEASUREMENTS AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING

No.	CHARACTERISTICS	evado.	SPEC. AND TEST	TEST	LIMITS		UNIT
INO.	CHARACTERISTICS	STIVIDOL	METHOD	CONDITIONS	MIN.	MAX.	UNIT
1	Zener Voltage	VZ	As per Table 2	As per Table 2	(1)	(2)	V
2	Reverse Current	I <sub>R</sub>	As per Table 2	As per Table 2	-	(3)	μА

#### **NOTES**

- 1. See Table 1(a), Column 4.
- 2. See Table 1(a), Column 5.
- 3. See Table 1(a), Column 10.



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#### **APPENDIX 'A'**

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#### AGREED DEVIATIONS FOR THOMSON-CSF

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
Para's 4.2.4 and 4.2.5	Deviations from Environmental and Endurance Tests (Chart IV) and from Lot Acceptance Tests (Chart V)
	<ol> <li>"Moisture Resistance", Para 9.16, according to MIL-STD-750, Method 1021, shal be replaced by "Climatic Sequence" according to IEC 68-1 with the following conditions:</li> </ol>
	- Phase 'D': Option 2, 5 cycles.
	- Phase 'F' (Low Air Pressure): Not applicable.
	<ol> <li>"Seal Test", Para's 9.8.1 and 9.8.2 according to MIL-STD-750, method 1071, Conditions 'C' and 'H', shall be replaced by "Seal Test" according to IEC 68-2-17, Test QI (Bomb Pressure Test) with the following conditions:</li> </ol>
	- The pressure within the vessel shall be 210N/cm <sup>2</sup> .
	- The duration of conditioning shall be 4 hours.
	<ul> <li>Final measurements: I<sub>R</sub> with the limits as specified in Table 2 of this Detail Specification.</li> </ul>
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