



**CAPACITORS, FIXED, CHIPS, CERAMIC
DIELECTRIC, TYPE I**

BASED ON TYPE 1812

ESCC Detail Specification No. 3009/005

Issue 9	June 2026
---------	-----------



LEGAL DISCLAIMER AND COPYRIGHT

European Space Agency, Copyright © 2026. All rights reserved.

The European Space Agency disclaims any liability or responsibility, to any person or entity, with respect to any loss or damage caused, or alleged to be caused, directly or indirectly by the use and application of this ESCC publication.

This publication, without the prior permission of the European Space Agency and provided that it is not used for a commercial purpose, may be:

- copied in whole, in any medium, without alteration or modification.
- copied in part, in any medium, provided that the ESCC document identification, comprising the ESCC symbol, document number and document issue, is removed.

DOCUMENTATION CHANGE NOTICE

(Refer to <https://escies.org> for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
1791	Specification updated to incorporate changes per DCR.

TABLE OF CONTENTS

1	GENERAL	5
1.1	SCOPE	5
1.2	APPLICABLE DOCUMENTS	5
1.3	TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS	5
1.4	THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS	5
1.4.1	The ESCC Component Number	5
1.4.2	Component Type Variants and Range of Components	7
1.5	MAXIMUM RATINGS	8
1.6	PHYSICAL DIMENSIONS	8
1.7	FUNCTIONAL DIAGRAM	8
2	REQUIREMENTS	9
2.1	GENERAL	9
2.1.1	Deviations from the Generic Specification	9
2.2	MARKING	9
2.3	ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES	10
2.3.1	Room Temperature Electrical Measurements	10
2.3.2	High and Low Temperatures Electrical Measurements	10
2.4	INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS	11
2.5	BURN-IN	12
	APPENDIX A	13
	APPENDIX B	14

1 GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics, and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. [3009](#).

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. [21300](#) shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 The ESCC Component Number

The ESCC Component Number shall be constituted as follows:

Example: 3009005012201JE

- Detail Specification Reference: 3009005
- Component Type Variant Number: 01 (as required)
- Characteristic code: Capacitance Value (2200pF): 2201 (as required)
- Characteristic code: Capacitance Tolerance ($\pm 5\%$): J (as required)
- Rating code: Rated Voltage (100V): E (as required)

1.4.1.1 Characteristics and Ratings Codes

Characteristics and ratings to be codified as part of the ESCC Component Number shall be as follows:

- (a) Rated Capacitance Value, C_n , expressed by means of the following codes in accordance with ESCC Basic Specification No. 21700. The unit quantity shall be picofarad (pF).

Capacitance Value C_n (pF)	Code
XXX	XXX0
XXX 10^1	XXX1
XXX 10^2	XXX2
XXX 10^3	XXX3

- (b) Capacitance Tolerance expressed by the following codes in accordance with ESCC Basic Specification No. 21700:

Tolerance (\pm)	Code Letter
1%	F
2%	G
5%	J
10%	K

- (c) Rated Voltage, U_R , expressed by the following codes:

Rated Voltage U_R (V)	Code Letter
16	X
25	A
50	C
100	E
200	G
500	L
1000	M

1.4.2 Component Type Variants and Range of Components

The component type variants and range of components applicable to this specification are as follows:

Variant Number	Style	Capacitance Range, Tolerance, Rated Voltage	Terminal Material and Finish		Weight Max (g)
			End Terminations	Termination Finish	
01	1812	See Note 1	Ag/Pd	No finish (Note 2)	0.3
03	1812	See Note 1	Ag/Pd/Pt	No finish (Note 2)	0.3
05	1812	See Note 1	Ag + Ni barrier	Sn60, Sn62 or Sn63 solder dip	0.3
06	1812	See Note 1	Ag + Ni barrier	Sn/Pb plating (Note 3)	0.3
08	1812	See Note 1	Ag + Ni barrier	Au plating (Note 2)	0.3

NOTES:

1. Available rated voltages, capacitance values and tolerances are as follows:

Rated Voltage U_R (V)	Capacitance Range C_n (pF)	
	Min	Max
1000	47	1800
500	47	3900
200	47	10000
100	100	15000
50	100	22000
25	100	27000
16	100	33000

Available Capacitance values for each Rated Voltage are as defined by the E12 to E96 series; however, any capacitance value within each specified capacitance range may be available on request.

Capacitance tolerances available are: 1% 2% 5% 10%.

2. Variants 01, 03 and 08 are not suitable for solder assembly methods. They shall be assembled using glue or wire bond techniques.
3. Sn/Pb plating with tin content of 50% minimum and 97% maximum, remainder lead.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

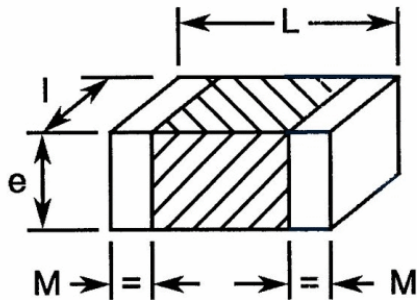
Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Maximum Ratings	Units	Remarks
Rated Voltage	U_R	16, 25, 50, 100, 200, 500, 1000	V	Note 1
Operating Temperature Range	T_{op}	-55 to +125	°C	Without derating. T_{amb}
Storage Temperature Range	T_{stg}	-55 to +125	°C	
Soldering Temperature	T_{sol}	+260	°C	Note 2

NOTES:

1. As required; See Para. 1.4.2.
2. Duration 10 seconds maximum.

1.6 PHYSICAL DIMENSIONS



Symbols	Dimensions (mm)			
	Variants 01, 03, 06, 08		Variant 05	
	Min	Max	Min	Max
L	4	5	4	5.5
l	2.8	3.6	2.8	4.1
e	-	2.8	-	3.3
M	0.2	1	0.2	1

1.7 FUNCTIONAL DIAGRAM



2 REQUIREMENTS

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

2.1.1.1 *Deviations from Qualification and Periodic Tests - Chart F4*

(a) Solderability: not applicable to Variants 01, 03 and 08.

2.2 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. [21700](#) and as follows.

The information to be marked on the component or its primary package shall be:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number (see Para. 1.4.1).
- (c) Traceability information.

2.3 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES

Electrical measurements shall be performed at room, high and low temperatures.

2.3.1 Room Temperature Electrical Measurements

The measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}\text{C}$.

Characteristics	Symbols	Test Method and Conditions	Tolerance (\pm %)	Limits		Units
				Min	Max	
Capacitance	C_A	ESCC No. 3009	1	$0.99C_n$	$1.01C_n$	pF
			2	$0.98C_n$	$1.02C_n$	
			5	$0.95C_n$	$1.05C_n$	
			10	$0.9C_n$	$1.1C_n$	
Tangent of Loss Angle	$\text{tg}\delta$	ESCC No. 3009	All	-	15×10^{-4}	-
Insulation Resistance	R_i	ESCC No. 3009 For $C_n \leq 10000\text{pF}$ For $C_n > 10000\text{pF}$	All	100	-	G Ω
				1000	-	G Ω .nF
Voltage Proof	VP	ESCC No. 3009	All	$2.5U_R$	-	V

2.3.2 High and Low Temperatures Electrical Measurements

Characteristics	Symbols	Test Method and Conditions (Note 1)	Limits		Units
			Min	Max	
Insulation Resistance	R_i	ESCC No. 3009 $T_{amb} = +125 \pm 2^{\circ}\text{C}$ Note 2 For $C_n \leq 10000\text{pF}$ For $C_n > 10000\text{pF}$	10	-	G Ω
			100	-	G Ω .nF
Temperature Coefficient	TC	ESCC No. 3009 $T_{amb} = -55 \pm 2^{\circ}\text{C}, +20 \pm 2^{\circ}\text{C}, +125 \pm 2^{\circ}\text{C}$ Note 3	-30	+30	$10^{-6}/^{\circ}\text{C}$

NOTES:

1. The measurements shall be performed on a sample of 5 components from each manufacturing lot with 0 failures allowed. In the event of any failure a 100% inspection may be performed.
2. Guaranteed but not tested during Chart F3 of the Generic Specification; only tested in Temperature Characterisation during Chart F4 of the Generic Specification.
3. In the case of a 100% inspection, a 1% total percent defective is allowed.

2.4 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Unless otherwise specified, the measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}C$.

Unless otherwise specified the test methods and test conditions shall be as per the corresponding test defined in Para. 2.3.1 Room Temperature Electrical Measurements.

Test Reference per ESCC No. 3009	Characteristics	Symbols	Limits		Units
			Min	Max	
Mounting Final Measurements	Capacitance Tangent of Loss Angle Insulation Resistance	C_A $tg\delta$ R_i	Record Values - 15×10^{-4} Note 1		-
Rapid Change of Temperature Initial Measurements	Capacitance	C_A	Notes 1, 2		
Final Measurements	Capacitance Change in Capacitance Tangent of Loss Angle	C_A $\Delta C_A/C_A$ $tg\delta$	Note 1 -1 +1 - 30×10^{-4}		% -
Steady State Humidity (85/85) Initial Measurements	Capacitance	C_A	Note 1		
Final Measurements (1000 hours)	Capacitance Change in Capacitance Tangent of Loss Angle Insulation Resistance (Note 3): For $C_n \leq 10000pF$ For $C_n > 10000pF$	C_A $\Delta C_A/C_A$ $tg\delta$ R_i R_i	Note 1 -2 +2 - 30×10^{-4} 10 - 100 -		% - GΩ GΩ.nF

Test Reference per ESCC No. 3009	Characteristics	Symbols	Limits		Units
			Min	Max	
Operating Life					
Initial Measurements	Capacitance	C_A	Notes 1, 2		
Intermediate Measurements (1000 hours) (Note 4)	Capacitance	C_A	Note 1		
	Change in Capacitance	$\Delta C_A/C_A$	-3	+3	%
	Insulation Resistance:				
	For $C_n \leq 10000\text{pF}$	R_i	10	-	$G\Omega$
For $C_n > 10000\text{pF}$	R_i	100	-	$G\Omega.nF$	
Final Measurements (1000 or 2000 hours) (Note 5)	Capacitance	C_A	Note 1		
	Change in Capacitance	$\Delta C_A/C_A$	-3	+3	%
	Tangent of Loss Angle	$\text{tg}\delta$	-	30×10^{-4}	-
	Insulation Resistance:				
	For $C_n \leq 10000\text{pF}$	R_i	10	-	$G\Omega$
	For $C_n > 10000\text{pF}$	R_i	100	-	$G\Omega.nF$
	Voltage Proof	VP	$2.5U_R$	-	V
Temperature Characterisation	Insulation Resistance at $T_{amb} = +125 \pm 2^\circ\text{C}$	R_i	Note 6		
	Temperature Coefficient	TC	Note 6		
Robustness of Terminations					
Final Measurements	Capacitance	C_A	Note 1		

NOTES:

- As specified in Para. 2.3.1 Room Temperature Electrical Measurements.
- Capacitance values recorded during Mounting may be used as initial measurements.
- Test conditions for Insulation Resistance shall be as specified in Steady State Humidity in the ESCC Generic Specification.
- Intermediate measurements are optional at the Manufacturer's discretion.
- 1000 hours is applicable to Periodic Testing for extension of qualification. 2000 hours is applicable to Qualification Testing, and to Periodic Testing for renewal of qualification after lapse.
- As specified in Para. 2.3.2 High and Low Temperatures Electrical Measurements.

2.5 BURN-IN

The requirements for Burn-in are specified in the ESCC Generic Specification. The following conditions shall also apply:

- After Burn-in, the components shall be removed from the chamber and allowed to cool under normal atmospheric conditions for recovery for 24 hours minimum.

APPENDIX A**AGREED DEVIATIONS FOR KYOCERA AVX COMPONENTS S.A.S. (F)**

Items Affected	Description of Deviations
Para. 2.1.1 Deviations from the Generic Specification: Screening Tests - Chart F3	High and Low Temperatures Electrical Measurements: Temperature Coefficient may be replaced with data provided by the ceramic material supplier, using Kyocera AVX production documents 1J-ICONTDIE-630L and 1J-ICONTDIE-900L (issues as per PID).

APPENDIX B**AGREED DEVIATIONS FOR EXXELIA TECHNOLOGIES (F)**

Items Affected	Description of Deviations
Para. 2.1.1 Deviations from the Generic Specification: Screening Tests - Chart F3	Burn-in: Due to the use of components of varying designs to cover the available range of components as listed in REP005 (ESCC QPL), for any particular component, the Applied Voltage for Burn-in shall be as specified in the PID as agreed with the ESCC Executive, with a minimum value of 2 times the Rated Voltage (DC).