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DIODES, MICROWAVE, SILICON,

TUNING VARACTOR,

BASED ON TYPES ML4355 THRU ML4365

ESCC Detail Specification No. 5512/007

ISSUE 1 October 2002



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TUNING VARACTOR,

BASED ON TYPES ML4355 THRU ML4365

ESA/SCC Detail Specification No. 5512/007

space components coordination group

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No. 5512/007

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APPENDICES (Applicable to specific Manufacturers only) 'A' Agreed Deviations for M/A-Com Ltd. (G.B.)

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1. <u>GENERAL</u>

1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics, test and inspection data for a Diode, Microwave, Silicon, Tuning Varactor, based on Types ML4355 thru ML4365. It shall be read in conjunction with ESA/SCC Generic Specification No. 5010, the requirements of which are supplemented herein.

1.2 <u>TYPE VARIANTS</u>

Variants of the basic diodes specified herein, which are also covered by this specification are given in Table 1(a).

1.3 MAXIMUM RATINGS

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the diodes specified herein, are as scheduled in Table 1(b).

1.4 PARAMETER DERATING INFORMATION

The derating information applicable to the diodes specified herein is shown in Figure 1.

1.5 PHYSICAL DIMENSIONS

The physical dimensions of the diodes specified herein are shown in Figure 2.

1.6 <u>FUNCTIONAL DIAGRAM</u>

The functional diagram, showing lead identification, of the diodes specified herein, is shown in Figure 3.

1.7 HANDLING PRECAUTIONS

These devices are susceptible to damage by electrostatic discharge. Therefore, suitable precautions shall be employed for protection during all phases of manufacture, testing, packaging, shipment and any handling.

These components are Categorised as Class 2 with a Minimum Critical Path Failure Voltage of 3400V.

2. <u>APPLICABLE DOCUMENTS</u>

The following documents form part of this specification and shall be read in conjunction with it:-

- (a) ESA/SCC Generic Specification No. 5010 for Discrete Microwave Semiconductor Components.
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices.

3. TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESA/SCC Basic Specification No. 21300 shall apply.



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TABLE 1(a) - TYPE VARIANTS

(1) VARIANT	(2) BASED ON TYPE	(3) FIGURE	(4) TOTAL CAPACITANCE C _T (pF)		(5) QUALITY FACTOR (Q) (MINIMUM)	(6) BODY-LID AND LEAD MATERIAL AND FINISH
			MIN	MAX	•	
01	ML4355 - 30	2(a)	1.90	2.50	850	A7-D2
02	ML4355 - 31	2(b)	1.90	2.50	850	A7-D2
. 03	ML4355 - 36	2(c)	1.90	2.50	850	A7-D2
04	ML4355 - 103	2(d)	2.00	2.60	850	A7-D2
05	ML4355 - 118	2(e)	1.94	2.54	850	A7
06	ML4355 - 186	2(f)	1.85	2.45	850	D2
07	ML4356 - 30	2(a)	2.30	3.10	850	A7-D2
08	ML4356 - 31	2(b)	2.30	3.10	850	A7-D2
09	ML4356 - 36	2(c)	2.30	3.10	850	A7-D2
10	ML4356 - 103	2(d)	2.40	3.20	850	A7-D2
11	ML4356 - 118	2(e)	2.34	3.14	850	A7
12	ML4356 - 186	2(f)	2.25	3.05	850	D2
13	ML4357 - 30	2(a)	2.85	3.75	800	A7-D2
14	ML4357 - 31	2(b)	2.85	3.75	800	A7-D2
15	ML4357 - 36	2(c)	2.85	3.75	800	A7-D2
16	ML4357 - 103	2(d)	2.95	3.85	800	A7-D2
17	ML4357 - 118	2(e)	2.89	3.79	800	A7
18	ML4357 - 186	2(f)	2.80	3.70	800	D2
19	ML4358 - 30	2(a)	3.40	4.40	750	A7-D2
20	ML4358 - 31	2(b)	3.40	4.40	750	A7-D2
21	ML4358 - 36	2(c)	3.40	4.40	750	A7-D2
22	ML4358 - 103	2(d)	3.50	4.50	750	A7-D2
23	ML4358 - 118	2(e)	3.45	4.45	750	A7
24	ML4358 - 186	2(f)	3.35	4.35	750	D2
25	ML4359 - 30	2(a)	4.10	5.30	700	A7-D2
26	ML4359 - 31	2(b)	4.10	5.30	700	A7-D2
27	ML4359 - 36	2(c)	4.10	5.30	700	A7-D2
28	ML4359 - 103	2(d)	4.20	5.40	700	A7-D2
29	ML4359 - 118	2(e)	4.14	5.34	700	A7
30	ML4359 - 186	2(f)	4.05	5.25	700	D2
31	ML4360 - 30	2(a)	4.90	6.30	700	A7-D2
32	ML4360 - 31	2(b)	4.90	6.30	700	A7-D2
33	ML4360 - 36	2(c)	4.90	6.30	700	A7-D2
34	ML4360 - 103	2(d)	5.00	6.40	700	A7-D2
35	ML4360 - 118	2(e)	4.94	6.34	700	A7
36	ML4360 - 186	2(f)	4.85	6.25	700	D2



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TABLE 1(a) - TYPE VARIANTS (CONT'D)

(1) VARIANT	(2) BASED ON TYPE	(3) FIGURE	(4) TOTAL CAPACITANCE C _T (pF)		(5) QUALITY FACTOR (Q) (MINIMUM)	(6) BODY-LID AND LEAD MATERIAL AND FINISH
			MIN	MAX	(initial cities)	
37	ML4361 - 30	2(a)	5.90	7.70	600	A7-D2
38	ML4361 - 31	2(b)	5.90	7.70	600	A7-D2
39	ML4361 - 36	2(c)	5.90	7.70	600	A7-D2
40	ML4361 - 103	2(d)	6.00	7.80	600	A7-D2
41	ML4361 - 118	2(e)	5.94	7.74	600	A7
42	ML4361 - 186	2(f)	5.85	7.65	600	D2
43	ML4362 - 30	2(a)	7.20	9.20	600	A7-D2
44	ML4362 - 31	2(b)	7.20	9.20	600	A7-D2
45	ML4362 - 36	2(c)	7.20	9.20	600	A7-D2
46	ML4362 - 103	2(d)	7.30	9.30	600	A7-D2
47	ML4363 - 30	2(a)	8.70	11.30	500	A7-D2
48	ML4363 - 31	2(b)	8.70	11.30	500	A7-D2
49	ML4363 - 36	2(c)	8.70	11.30	500	A7-D2
50	ML4363 - 103	2(d)	8.80	11.40	500	A7-D2
51	ML4364 - 30	2(a)	10.60	13.40	500	A7-D2
52	ML4364 - 31	2(b)	10.60	13.40	500	A7-D2
53	ML4364 - 36	2(c)	10.60	13.40	500	A7-D2
54	ML4364 - 103	2(d)	10.70	13.50	500	A7-D2
55	ML4365 - 30	2(a)	13.00	17.00	500	A7-D2
56	ML4365 - 31	2(b)	13.00	17.00	500	A7-D2
57	ML4365 - 36	2(c)	13.00	17.00	500	A7-D2
58	ML4365 - 103	2(d)	13.10	17.10	500	A7-D2



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TABLE 1(b) - MAXIMUM RATINGS

No.	CHARACTERISTIC	SYMBOL	MAXIMUM RATINGS	UNIT	REMARKS
1	D.C. Reverse Voltage	V _R	- 60	V	Note 1
2	Operating Temperature Range	Т _{ор}	-65 to +150	°C	T _{amb}
3	Storage Temperature Range	T _{stg}	-65 to +150	°C	
4	Soldering Temperature	T _{sol}	+ 230	°C	Note 2

NOTES

- 1. Measured at $I_R = 10\mu$ A and $T_{amb} = +25$ °C. For derating at $T_{amb} > +25$ °C, see Figure 1.
- 2. Duration 5 seconds maximum (at a distance of not less than 1.5mm from the body for Variants 06, 12, 18, 24, 30, 36 and 42) and the same termination shall not be resoldered until 3 minutes have elapsed.

FIGURE 1 - PARAMETER DERATING INFORMATION





FIGURE 2 - PHYSICAL DIMENSIONS



┝

--L-→|<-



			B1	←	
ØD1 Ød	B2→	· · ·			ØD
		Cathode end			I → Y1

──B───>**|**<──L->|



SYMBOL	MILLIM	ETRES	
STMBOL	MIN	MAX	
А	5.20	5.72	
В	2.16	2.46	
B1	0.41	0.61	
B2	0.15	0.25	
Ød	1.52	1.63	
ØD	3.00	3.23	

1.95

1.52

2.11

1.63

ØD1

L

FIGURE 2(b) - VARIANTS 02, 08, 14, 20, 26, 32, 38, 44, 48, 52, 56



SYMBOL.	MILLIM	ETRES
	MIN	MAX
А	2.16	2.46
B1	0.41	0.61
B2	0.15	0.25
ØD	3.00	3.23
ØD1	1.95	2.11







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FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)

FIGURE 2(c) - VARIANTS 03, 09, 15, 21, 27, 33, 39, 45, 49, 53, 57



SYMBOL	MILLIM	ETRES
STIVIDUL	MIN	MAX
А	3.60	4.18
В	2.16	2.46
B1	0.41	0.61
B2	0.15	0.25
Ød	1.52	1.63
ØD	3.00	3.23
ØD1	1.95	2.11
L	1.52	1.63







FIGURE 2(d) - VARIANTS 04, 10, 16, 22, 28, 34, 40, 46, 50, 54, 58





FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)

FIGURE 2(e) - VARIANTS 05, 11, 17, 23, 29, 35, 41



SYMBOL	MILLIM	ETRES
STMBUL	MIN	MAX
А	4.19	4.70
В	0.77	1.04
B1	0.20	0.30
B2	0.22	0.28
С	2.21	3.29
C1	0.38	0.64
Ød	1.22	1.32
ØD	2.49	2.59
ØD1	1.60	2.00
Q	0.64	1.14
V	0.64	0.94







FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)





NOTES

1. The cathode end shall be marked with a black dot or band. The marking will not be on the cathode connection but adjacent to it.



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4. **REQUIREMENTS**

4.1 <u>GENERAL</u>

The complete requirements for procurement of the diodes specified herein shall be as stated in this specification and ESA/SCC Generic Specification No. 5010 for Discrete Microwave Semiconductor Components. Deviations from the Generic Specification applicable to this specification only, are listed in Para. 4.2.

Deviations from the applicable Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESA/SCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

4.2 DEVIATIONS FROM GENERIC SPECIFICATION

4.2.1 Deviations from Special In-process Controls

- (a) Para. 5.2.3, Total Dose Irradiation Testing: Shall be performed during qualification and extension of qualification.
- (b) Para. 5.2.3, Total Dose Irradiation Testing: Shall be performed during procurement on a lot acceptance basis at the total dose irradiation level specified in the purchase order.
- (c) Quality Factor Measurement (Q): 3 randomly chosen samples shall be encapsulated in the type of package shown in Figure 2(a) of this specification. After encapsulation, Quality Factor measurements shall be performed using the method shown in Figure 4 of this specification with $V_{\rm R} = -4.0V$. The limits specified in Table 1(a) of this specification shall be met.

4.2.2 <u>Deviations from Final Production Tests (Chart II)</u>

None.

4.2.3 Deviations from Burn-in and Electrical Measurements (Chart III)

(a) Para. 9.21, High Temperature Reverse Bias Burn-in: Shall be performed at 50% of rated V_B.

4.2.4 Deviations from Qualification Tests (Chart IV)

(a) Para. 9.23, Special Testing: Shall not be performed.

- 4.2.5 <u>Deviations from Lot Acceptance Tests (Chart V)</u>(a) Para. 9.23, Special Testing: Shall not be performed.
- 4.3 MECHANICAL AND ENVIRONMENTAL REQUIREMENTS
- 4.3.1 Dimension Check

The dimensions of the diodes specified herein shall be checked. They shall conform to those shown in Figure 2.



4.3.2 Weight

The maximum weight of the diodes specified herein shall be:

Variant No.	Weight (g)
01, 07, 13, 19, 25, 31, 37, 43, 47, 51, 55	0.12
02, 08, 14, 20, 26, 32, 38, 44, 48, 52, 56	0.06
03, 09, 15, 21, 27, 33, 39, 45, 49, 53, 57	0.10

Variant No.	Weight (g)
04, 05, 10, 11, 16, 17, 22, 23, 28, 29, 34, 35, 40, 41, 46, 50, 54, 58	0.14
06, 12, 18, 24, 30, 36, 42	0.06

4.3.3 <u>Terminal Strength</u>

The requirements for terminal strength testing are specified in Section 9 of ESA/SCC Generic Specification No. 5010. The test conditions shall be as follows:-

(a) Condition: 'A' (Tension)

Variants 06, 12, 18, 24, 30, 36 and 42:

- Force: 5.1N.
- Duration: 5 seconds.

(b) Condition: 'D2' (Stud Torque)

Variants 04, 10, 16, 22, 28, 34, 40. 46, 50, 54 and 58:

- Torque: 56mNm.
- Duration: 5 seconds.

Variants 05, 11, 17, 23, 29, 35 and 41:

- Torque: 42mNm.
- Duration: 5 seconds.

(c) Condition: Compression

Variants 01, 02, 03, 04, 07, 08, 09, 10, 13, 14, 15, 16, 19, 20, 21, 22, 25, 26, 27, 28, 31, 32, 33, 34, 37, 38, 39, 40, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57 and 58:

- Force: 50N.
- Duration: 5 seconds.

Variants 05, 11, 17, 23, 29, 35 and 41:

- Force: 10N.
- Duration: 5 seconds.

The compression test shall be performed by applying the specified force to the end-cap by means of a suitable weight applied for the specified time. On completion of the test, a visual examination shall be performed to check for damage to the end-cap or the ceramic body.



4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the diodes specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material shall not guarantee acceptance of the finished product.

4.4.1 <u>Case</u>

The case shall be hermetically sealed and have a ceramic body. The lid shall be brazed, welded or preform soldered.

4.4.2 Lead Materials and Finish

- (a) For Variants 01, 02, 03, 04, 07, 08, 09, 10, 13, 14, 15, 16, 19, 20, 21, 22, 25, 26, 27, 28, 31, 32, 33, 34, 37, 38, 39, 40, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57 and 58, the body material shall be Type 'A' with Type '7' finish and the lid material shall be Type 'D' with Type '2' finish, in accordance with the requirements of ESA/SCC Basic Specification No. 23500.
- (b) For Variants 05, 11, 17, 23, 29, 35 and 41, the lead material shall be Type 'A' with Type '7' finish in accordance with the requirements of ESA/SCC Basic Specification No. 23500.
- (c) For Variants 06, 12, 18, 24, 30, 36 and 42, the lead material shall be Type 'D' with Type '2' finish in accordance with the requirements of ESA/SCC Basic Specification No. 23500.

4.5 MARKING

4.5.1 General

The marking of components delivered to this specification shall be in accordance with the requirements of ESA/SCC Basic Specification No. 21700 and the following paragraphs. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany the component in its primary package.

The information to be marked and the order of precedence, shall be as follows:-

- (a) Cathode Identification.
- (b) The SCC Component Number.
- (c) Traceability Information.

4.5.2 Cathode Identification

Cathode identification shall be as shown in Figures 2 and 3 of this specification.

4.5.3 The SCC Component Number

Each component shall bear the SCC Component Number which shall be constituted and marked as follows:

	551200701	۵ŀ
Detail Specification Number		Π
Type Variant (see Table 1(a))		
Testing Level (B or C, as applicable)		1
Total Dose Irradiation Level (if applicable)		

The Total Dose Irradiation Level designation shall be added for those devices for which a sample has been successfully tested to the level in question. For these devices, a code letter shall be added in accordance with the requirements of ESA/SCC Basic Specification No. 22900.



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4.5.4 Traceability Information

Each component shall be marked in respect of traceability information as defined in ESA/SCC Basic Specification No. 21700.

4.6 ELECTRICAL MEASUREMENTS

4.6.1 <u>Electrical Measurements at Room Temperature</u>

The parameters to be measured at room temperature are scheduled in Table 2. Unless otherwise specified, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.6.2 <u>Electrical Measurements at High and Low Temperatures</u>

The parameters to be measured at high and low temperatures are scheduled in Table 3. Unless otherwise specified, the measurements shall be performed at $T_{amb} = +150(+0-3)$ °C.

4.6.3 <u>Circuits for Electrical Measurements</u>

A circuit for use in performing the quality factor measurement is shown in Figure 4.

4.7 BURN-IN TESTS

Burn-in shall be Category 1 of Chart III(a).

4.7.1 Parameter Drift Values

The parameter drift values applicable to burn-in are specified in Table 4 of this specification. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C. The parameter drift values (Δ) applicable to the scheduled parameters shall not be exceeded. In addition to these drift value requirements for a given parameter, the appropriate limit value specified in Table 2 shall not be exceeded.

4.7.2 Conditions for High Temperature Reverse Bias Burn-in

The requirements for the high temperature reverse bias burn-in are specified in Section 7 of ESA/SCC Generic Specification No. 5010. The conditions for high temperature reverse bias burn-in shall be as specified in Table 5(a) of this specification.

4.7.3 Conditions for Power Burn-in

The requirements for power burn-in are specified in Section 7 of ESA/SCC Generic Specification No. 5010. The conditions for power burn-in shall be as specified in Table 5(b) of this specification.

4.7.4 Electrical Circuits for High Temperature Reverse Bias and Power Burn-in

Circuits for use in performing the H.T.R.B and power burn-in tests are shown in Figures 5(a) and 5(b) of this specification.



TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - D.C. PARAMETERS

No.	CHARACTERISTICS SYMBOL	MIL-STD-750	TEST CONDITIONS	LIMITS		UNIT		
	140.		STWDUL	TEST METHOD		MIN.	MAX.	UNIT
	1	Reverse Current 1	I _{R1}	4016	V _R = -60V	-	10	μΑ
	2	Reverse Current 2	I _{R2}	4016	V _R = -30V	-	50	nA
	3	Forward Voltage	V _F	4011	l _F = 100mA	-	1.0	V

TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - A.C. PARAMETERS

No.	CHARACTERISTICS	SYMBOL .	MIL-STD-750	TEST	LIMITS		- UNIT
		UTIMBOL	TEST METHOD	DD CONDITIONS MIN. MAX		MAX.	
4	Total Capacitance	CT	4001	V _R = -4.0V f = 1.0MHz	Not	e 1	pF
5	Total Capacitance Ratio	-	4001	$V_{R} = 0V$ $V_{R} = -60V$ $f = 1.0MHz$ Note 2 Variants 01 to 06 Variants 07 to 12 Variants 13 to 18 Variants 19 to 24 Variants 25 to 30 Variants 31 to 36 Variants 37 to 42 Variants 43 to 46 Variants 47 to 50 Variants 51 to 54 Variants 55 to 58	5.8 6.1 6.4 6.5 6.7 6.8 6.9 7.0 7.1 7.2 7.3	- - - - - - - - - - - -	

NOTES

1. See Column 4 of Table 1(a).

2. Ratio =
$$\frac{C_T \text{ at } V_B = 0V}{C_T \text{ at } V_R = -60V}$$



TABLE 3 - ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES

No.	CHARACTERISTICS	SYMBOI SPEC.AND/OR TEST	TEST	LIMITS		UNIT	
	NO. CHARACTERISTICS	OTMOOL	TEST METHOD	CONDITIONS	MIN.	MAX.	UNIT
2	Reverse Current 2	I _{R2}	As per Table 2	As per Table 2	-	10	μΑ

TABLE 4 - PARAMETER DRIFT VALUES

No.	CHARACTERISTICS	SYMBOL	SPEC.AND/OR TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
2	Reverse Current 2	I _{R2}	As per Table 2	As per Table 2	± 10 (1) or (2) ± 100 (1)	nA %
3	Forward Voltage	V _F	As per Table 2	As per Table 2	± 100 (1)	mV

NOTES

1. $\Delta 1 = \Delta 2$.

2. Whichever is the greater, referred to the initial measurement.



FIGURE 4 - CIRCUITS FOR ELECTRICAL MEASUREMENTS





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TABLE 5(a) - CONDITIONS FOR HIGH TEMPERATURE REVERSE BIAS BURN-IN AND OPERATING LIFE TESTS

No.	CHARACTERISTICS	CTERISTICS SYMBOL		UNIT
1	Ambient Temperature	T _{amb}	+ 150(+ 0 - 3)	°C
2	Reverse Voltage	V _R	- 30	V

TABLE 5(b) - CONDITIONS FOR POWER BURN-IN

No.	CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT
1	Ambient Temperature	T _{amb}	+ 125(+ 0 - 3)	°C
2	Forward Current	ļÈ	25	mA

FIGURE 5(a) - ELECTRICAL CIRCUIT FOR HIGH TEMPERATURE REVERSE BIAS BURN-IN AND OPERATING LIFE TESTS



FIGURE 5(b) - ELECTRICAL CIRCUIT FOR POWER BURN-IN





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4.8 <u>ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESA/SCC GENERIC</u> SPECIFICATION NO. 5010)

4.8.1 <u>Electrical Measurements on Completion of Environmental Tests</u>

The parameters to be measured on completion of environmental tests are scheduled in Table 2. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.8.2 <u>Electrical Measurements at Intermediate Points and on Completion of Endurance Tests</u>

The parameters to be measured at intermediate points and on completion of endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.8.3 <u>Conditions for Operating Life Tests (Part of Endurance Testing)</u>

The requirements for operating life testing are specified in Section 9 of ESA/SCC Generic Specification No. 5010. The conditions for operating life testing are specified in Table 5(a) of this specification.

4.8.4 <u>Electrical Circuits for Operating Life Tests</u>

The circuit to be used for performance of the operating life test shall be the same as shown in Figure 5(a) for High Temperature Reverse Bias Burn-in.

4.9 TOTAL DOSE IRRADIATION TESTING

4.9.1 Application

If specified in Para. 4.2.1 of this specification, total dose irradiation testing shall be performed in accordance with the requirements of ESA/SCC Basic Specification No. 22900.

4.9.2 Bias Conditions

Continuous bias shall be applied during irradiation testing as shown in Figure 6 of this specification.

4.9.3 <u>Electrical Measurements</u>

The parameters to be measured prior to irradiation exposure are scheduled in Table 2 of this specification. Only devices which meet the requirements of Table 2 shall be included in the test sample.

The parameters to be measured during and on completion of irradiation testing are scheduled in Table 7 of this specification.

4.10 SPECIAL TESTING

Not applicable.



TABLE 6 - ELECTRICAL MEASUREMENTS AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	LIMITS	UNIT
NO.					MIN. MAX.	
1	Reverse Current 1	I _{R1}	As per Table 2	As per Table 2	As per Table 2	μA
2	Reverse Current 2	I _{R2}	As per Table 2	As per Table 2	As per Table 2	nA
3	Forward Voltage	V _F	As per Table 2	As per Table 2	As per Table 2	v
4	Total Capacitance	CT	As per Table 2	As per Table 2	As per Table 2	ρF

FIGURE 6 - BIAS CONDITIONS FOR IRRADIATION TESTING



NOTES

1. A reverse bias of $V_R = -30V$, shall be applied.



TABLE 7 - ELECTRICAL MEASUREMENTS DURING AND ON COMPLETION OF IRRADIATION TESTING

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
2	Reverse Current 2	I _{R2}	As per Table 2	As per Table 2	Note 1	nA

NOTES

1. The graph given below shall be used to determine the maximum permitted change.





APPENDIX 'A'

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AGREED DEVIATIONS FOR M/A-Com LTD. (G.B.)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
Para. 4.2.2	Para. 9.4, "High Temperature Stabilisation Bake": May be performed at +150(+0-3) °C.