DIODES, MICROWAVE, SILICON, PIN

BASED ON TYPES BXY43C AND BXY44K

ESCC Detail Specification No. 5513/030

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DOCUMENTATION CHANGE NOTICE

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DCR No.	CHANGE DESCRIPTION
1035	Specification upissued to incorporate changes per DCR.

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1 GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

(a) ESCC Generic Specification No. 5010
(b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 The ESCC Component Number

The ESCC Component Number shall be constituted as follows:

Example: 551303001

- Detail Specification Reference: 5513030
- Component Type Variant Number: 01 (as required)

1.4.2 Component Type Variants

The component type variants applicable to this specification are as follows:

Variant Number	Based on Type	Case	Terminal Material and Finish		Weight max g
			Cathode	Anode	
01	BXY43C	T	D2	E2	0.02
02	BXY43C	T1	E2	E2	0.02
05	BXY44K	T	D2	E2	0.02
06	BXY44K	T1	E2	E2	0.02

The terminal material and finish shall be in accordance with the requirements of ESCC Basic Specification No. 23500.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Maximum Ratings	Units	Remarks
DC Reverse Voltage Variants 01, 02 Variants 05, 06	V_R	 -150 -200	V	
Forward Current (Continuous)	I_F	400	mA	
Power Dissipation Variant 01 Variant 02 Variant 05 Variant 06	P_D	 500 500 500 500	mW	 $T_{case} \leq +100°C$ $T_{case} \leq +88°C$ $T_{case} \leq +95°C$ $T_{case} \leq +80°C$ Note 1
Operating Temperature Range	T_{op}	-55 to +150	°C	T_{case}
Storage Temperature Range	T_{stg}	-65 to +175	°C	
Soldering Temperature	T_{sol}	+235	°C	Note 2
Junction Temperature	T_j	+150	°C	
Thermal Resistance, Junction-to-Case Variant 01 Variant 02 Variant 05 Variant 06	$R_{th(j-c)}$	 100 125 110 140	°C/W	

NOTES:

1. For T_{case} greater than specified, derate linearly to 0W at T_{case} = +150°C.
2. Duration 5 seconds maximum and the same termination shall not be resoldered until 5 minutes have elapsed.

1.6 HANDLING PRECAUTIONS

These devices are susceptible to damage by electrostatic discharge. Therefore suitable precautions shall be employed for protection during all phases of manufacture test, packaging, shipping and handling.

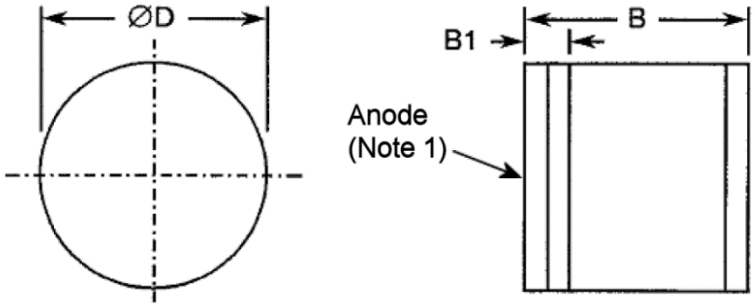
These components are categorised as follows:

Variants 01 and 02 are Class 1 per ESCC Basic Specification No. 23800 with a Minimum Critical Path Failure Voltage of 550V.

Variants 05 and 06 are Class 2 per ESCC Basic Specification No. 23800 with a Minimum Critical Path Failure Voltage of 1.2kV.

1.7 PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION

1.7.1 Case Type T

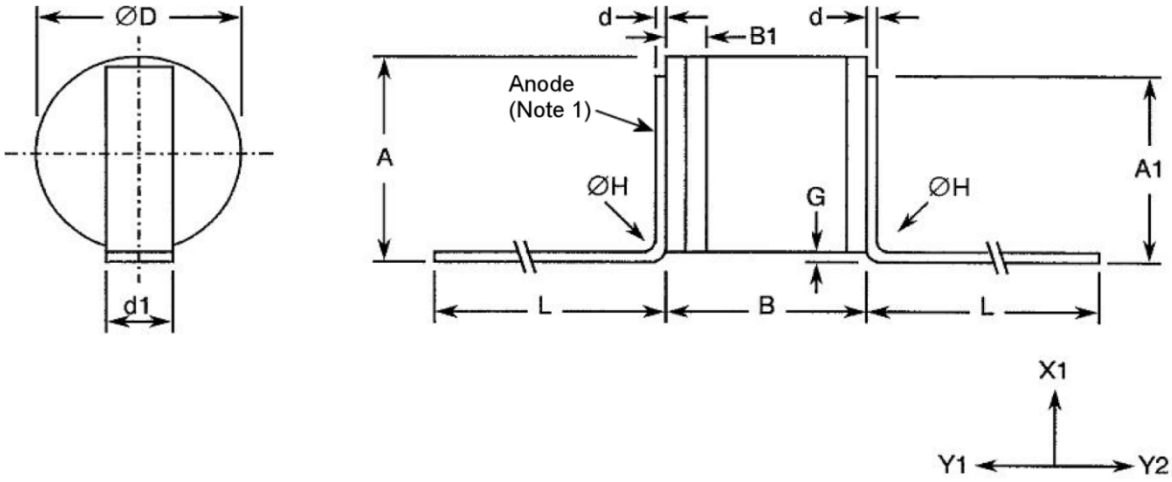


Symbols	Dimensions mm		Notes
	Min	Max	
B	1.15	1.35	
B1	-	0.4	1
ØD	1.3	1.45	

NOTES:

1. The anode terminal is identified by the sealing ring and lid (dimension B1).

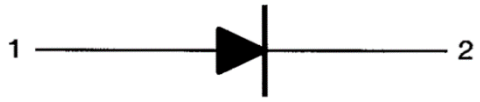
1.7.2 Case Type T1



Symbols	Dimensions mm		Notes
	Min	Max	
A	1.4	1.95	
A1	1.05	1.25	
B	1.15	1.35	
B1	-	0.4	1
d	0.06	0.1	
d1	0.4	0.6	
ØD	1.3	1.45	
G	0.1	0.5	
ØH	-	0.3	
L	5.5	-	

NOTES:
1. The anode terminal is identified by the sealing ring and lid (dimension B1).

1.8 FUNCTIONAL DIAGRAM



1. Anode
2. Cathode

1.9 MATERIALS AND FINISHES

Materials and finishes shall be as follows:

(a) Case
The case shall be hermetically sealed, have a ceramic body and a metal sealing ring and lid.

(b) Terminals
As specified in Component Type Variants.

2 REQUIREMENTS

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

2.1.1.1 Deviations from Screening Tests (Chart F3)

(a) Radiographic Inspection: shall be performed in the X and Z axes only.

2.1.1.2 Deviations from Qualification and Periodic Tests for Packaged Components (Chart F4A)

(a) Mechanical Shock: Not applicable.
(b) Vibration: Not applicable.
(c) Constant Acceleration: Not applicable.

2.2 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700. The information to be marked and the order of precedence shall be as follows:

(a) The ESCC qualified components symbol (for ESCC qualified components only).
(b) The ESCC Component Number.
(c) Traceability information.

2.3 DIE SHEAR

In those cases where package clearances are such that a die shear test is not practicable, the die shall be pushed away with a suitable tool. The force required to remove the die need not be recorded. The die attachment area shall be inspected and the component shall be considered acceptable if more than 50% of the semiconductor material remains.

2.4 TERMINAL STRENGTH

The test conditions for terminal strength, tested as specified in the ESCC Generic Specification, shall be as follows:

- Variants 02, 06: Test Condition A, tension, with a force of 1.5N and a duration of 5s.

2.5 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES

2.5.1 Room Temperature Electrical Measurements

The measurements shall be performed at T_{amb} = +25 ±3°C.

Characteristics	Symbols	MIL-STD-750 Test Method	Test Conditions	Limits		Units
				Min	Max	
Reverse Current 1	I_{R1}	4016	V_R = -150V Variants 01, 02 V_R = -200V Variants 05, 06	 - -	 100 100	nA
Reverse Current 2	I_{R2}	4016	V_R = -100V Variants 01, 02 Variants 05, 06	 - -	 10 5	nA
Forward Voltage	V_F	4011	I_F = 100mA Variants 01, 02 Variants 05, 06	 - -	 1 1.05	V
Total Capacitance	C_T	4001	V_R = -50V, f = 1MHz Variants 01, 02 Variants 05, 06	 - -	 0.45 0.35	pF
Forward Resistance 1	r_{f1}	4056	f = 100MHz I_{F1} = 20µA Variants 01, 02 I_{F1} = 10µA Variants 05, 06	 - 800	 70 1300	Ω
Forward Resistance 2	r_{f2}	4056	f = 100MHz I_{F2} = 1mA Variants 01, 02 Variants 05, 06	 - 12	 3 28	Ω
Forward Resistance 3	r_{f3}	4056	f = 100MHz I_{F3} = 10mA Variants 01, 02 Variants 05, 06	 - 2	 1.5 5	Ω
Charge Carrier Lifetime	τ	-	I_F = 10mA, I_R = -6mA Note 1 Variants 01, 02 Variants 05, 06	 250 300	 - -	ns

NOTES:

1. Pulsed measurement, duration = 1µs, duty cycle ≤ 33%. The input pulse shall be provided by a suitable pulse generator with tr ≤ 5ns.
   τL is measured across the 50% I_R points of the output waveform.

2.5.2 High and Low Temperatures Electrical Measurements

The measurements shall be performed only at T_{amb} = +100 (+0 -5)°C.

Characteristics	Symbols	MIL-STD-750 Test Method	Test Conditions (Note 1)	Limits		Units
				Min	Max	
Reverse Current 2	I_{R2}	4016	V_R = -100V	-	250	nA

NOTES:

1. Measurements shall be performed on a sample of 5 components. In the event of any failure a 100% inspection shall be performed.

2.6 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} = +25 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Limits			Units
		Drift Value (1) Δ	Absolute		
			Min	Max	
Reverse Current 2	I_{R2}	±3			nA
Variants 01, 02			-	10	
Variants 05, 06			-	5	
Forward Voltage	V_F	±0.03			V
Variants 01, 02			-	1	
Variants 05, 06			-	1.05	
Forward Resistance 2	r_{f2}	±15%			Ω
Variants 01, 02			-	3	
Variants 05, 06			12	28	

NOTES:

1. Δ1 = Δ2.

2.7 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Unless otherwise specified, the measurements shall be performed at T_{amb} = +25 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Limits		Units
		Min	Max	
Reverse Current 1	I_{R1}	-	100	nA
Reverse Current 2	I_{R2}			nA
Variants 01, 02		-	10	
Variants 05, 06		-	5	
Forward Voltage	V_F			V
Variants 01, 02		-	1	
Variants 05, 06		-	1.05	

2.8 BURN-IN 1 CONDITIONS

Characteristics	Symbols	Test Conditions	Units
Ambient Temperature	T_{amb}	+150 (+0 -5)	°C
Reverse Voltage	V_R		V
Variants 01, 02		-120	
Variants 05, 06		-160	
		Note 1	

NOTES:

1. After the burn-in period the components shall be allowed to cool until T_{amb} = +25 ±3°C and the V_R bias shall be maintained until T_{amb} < +35°C.

2.9 BURN-IN 2 CONDITIONS

Characteristics	Symbols	Test Conditions (Note 1)	Units
Junction Temperature	T_j	+150 (+0 -5)	°C
Power Dissipation	P_D	490 ±10	mW

NOTES:

1. T_{case} shall be adjusted to attain the specified T_j.

2.10 OPERATING LIFE CONDITIONS

The conditions shall be as specified for Burn-in 2.

APPENDIX A

AGREED DEVIATIONS FOR INFINEON TECHNOLOGIES AG (D)

Items Affected	Description of Deviations
Deviations from Generic Specification: Special In-Process Controls (Chart F2)	Bond Strength: The following pre-seal bond strength shall apply: • 0.05N minimum
	Dimension Check: May be performed during Chart F3 testing.
Deviations from Generic Specification: Screening Tests (Chart F3)	The following additional screening may be performed at any point prior to initial Room Temperature Electrical Measurements in Chart F3: Pre-Burn-in in accordance with MIL-STD-750, Test Method 1039, Test Condition A, with other Test Conditions as specified in Burn-in 1 Conditions herein. • Duration: ≥ 72 hours.
	Temperature Cycling: Shall be replaced by a Thermal Shock test in accordance with MIL-STD-202, Test Method 107, Test Condition B, 20 cycles.
Deviations from Generic Specification: Qualification and Periodic Tests (Chart F4)	Temperature Cycling: Shall be replaced by a Thermal Shock test in accordance with MIL-STD-202, Test Method 107, Test Condition B, 100 cycles.
	Assembly Capability Subgroup tests: In addition to the permitted use of empty packages or electrical rejects as test samples, components rejected during the following Screening Tests: • Radiographic Inspection • Seal • External Visual Inspection may be used on the condition that the cause for rejection has no possible impact on the tests, and they have been subjected to the same screening as the packages of the assembly lot with which they are associated.
	Bond Strength: The following post-seal bond strength shall apply: • 0.04N minimum
Deviations from Generic Specification: Final Customer Source Inspection	Final Customer Source Inspection shall be limited to witnessing of the DC parameters specified in Room Temperature Electrical Measurements.
Deviations from Generic Specification: Data Documentation	Additional Documentation and Wafer Lot Acceptance Data: If Wafer Lot Acceptance Data is stipulated in the Purchase Order, such data will not be delivered but will be available for review at Infineon Technologies AG.