



DOCUMENT CHANGE REQUEST

DCR number	742	Changes required for:	General	Originator:	Steve Thacker
Date:	2015/07/14	Date sent:	2012/06/18	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Capacitors Fixed Chip High Voltage (1-3KV) Ceramic Dielectric Type II, based on types 1812 & 1825

Number: 3009/034 Issue: 2

Other documents affected:

Page:

Total reformat/re-write of ESCC Detail Specification 3009/034 issue 2 as part of the ongoing conversion of legacy ESA/SCC specifications to the ESCC format as well as reflecting changes resulting from the conversion of ESCC Generic Specification No. 3009 issue 1 (per DCR695 & also DCR706).

The layout, format and general content of 3009/034 issue 3 is based on other converted ESCC Detail Specifications (see attached for proposed 3009/034 issue 3 Draft A).

The technical content of ESCC 3009/034 issue 3 remains closely based on the original ESCC 3009/034 issue 2 except as detailed herein.

Paragraph:

see below

Original wording:

see original ESCC 3009/034 issue 2

Proposed wording:

Total reformat of this Detail Specification (from the range of various ESCC Detail Specifications, 3009/xxx, for capacitors under Generic Specification No. 3009) as part of the ongoing conversion to the ESCC format.

See below for summary of changes, also see attached the proposed 3009/034 Issue 2 Draft A.

Note: known support for active procurement against this specification includes the following Manufacturers:

- AVX LTD (GB) (ESCC Qualified)

Summary of changes to the current format, layout and content is as follows:

1) General

Rewording and restructure of various sections and paragraphs of the specification, plus other editorial changes based on the layout and editorial content of other Detail Specifications already converted to ESCC format.

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In addition, editorial and technical amendments resulting from the changes made to the test requirements of the Generic Specification No. 3009.

Specific amendments include:

- Table 3: Delete Insulation Resistance, and Temperature Characteristic for each capacitance value [Test No. 5(i)] (due to SCC level B being deleted)
- Para 4.2.2: Delete deviation on preconditioning which is now covered by the Generic Spec
- Para. 4.7.1 & Table 4 is deleted (due to SCC level B being deleted)
- Table 6 only includes specified electrical measurement requirements as required for the various Chart F4 Qual level tests (editorial change)
- Table 6: Adhesion is renamed as Robustness of Terminations
- Table 6: add note that the capacitance values recorded during Mounting may be used as initial measurements for other tests (to provide the same flexibility included in 3009 issue 1)

2) Table 1(a), Figure 2, Para 4.3.2, Para 4.4.1, Figure 2, (Para 1.4.2 in issue 3)

All component range and selection options (including Variant, style, capacitance, tolerance, voltage, dimensions and terminal materials, weight) are brought together under a single Para. (editorial changes for clarification purposes)

3) Table 1(b): remark against Operating Temperature range is added (for clarification)

4) Para 4.4.1: Solder coating definition is simplified to be the industry standard definition: Sn62 (in Para 1.4.2 in issue 3)

5) Table 2 & Table 6: Units for Insulation Resistance are rewritten as Mohm.uF (instead of 's')

6) Table 2: Add note on capacitance limits prior to Burn-in (to be consistent with other 3009/xxx type II specs)

7) Table 3 Temperature Coefficient - Test No. 5(ii) (Para 2.3.2 in issue 3)

Amend remark to read "... 5 components from each manufacturing lot ..." (instead of "5 parts for each fired ceramic lot") (to be consistent with ESCC terminology & the Generic spec).

8) Table 5 is deleted as all conditions are already specified in the Generic spec (Temperature & Voltage).

Justification:

