DOCUMENT CHANGE REQUEST

DCR number 1762	Changes required for: General	Originator: Steve Thacker
Date: 2026/01/28	Date sent: 2025/07/30	Organisation: ESCC Executive Secretariat
Status: IMPLEMENTED		

Title:	Generic Specification for Capacitors Fixed Chips Ceramic Dielectric Types I and II		
Number:	3009	Issue:	6

Other documents affected:

3012-5

Page:

Se attached spec mark-ups

Paragraph:

Chart F3

Original wording:

AS per current issue

Proposed wording:

The order of the post-Burn-in electrical measurements within Chart F3 are to be reversed.
See attached spec mark-ups for details.
i.e.
Chart F3/Screening order to be changed to be:
1: Burn-in ->
2: High and Low Temperatures Electrical Measurements ->
3: Room Temperature Electrical Measurements ->

Justification:

As discussed with Manufacturer Exxelia in reference to their similar DCR1738 raised on all other ESCC capacitor Generic specifications (i.e. 3001, 3003, 3006, 3008, 3010), it was agreed with Exxelia that the most suitable order for the post Burn-in Electrical measurements actually should be as proposed above. i.e.:
: Burn-in -> sample High & Low Temps Electrical -> 100% Room Temp Electrical
Accordingly, DCR1738 should be withdrawn and replaced by this DCR.

My justification/explanation for this DCR:

In ESCC 3009 & 3012, the 5 part sample High & Low Temps Electrical is positioned in Chart F3 after the 100% Room Temp Electrical; this is different to all the other ESCC Capacitor Generic specs (i.e. 3001, 3003, 3006, 3008, 3010) where the order is reversed.
i.e. the 'usual' ESCC test sequence has the 100% Room Temp test as the final electrical before the parts are delivered to customers.

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Note: The reason the 3009 (& also 3012) has the different Chart F3 test order:
: Burn-in -> 100% Room Temp Electrical -> sample High & Low Temps Electrical
is due to the PSWG review of DCR695 in 2014 where the PSWG specifically reversed this test order (no justification/explanation for this change is available).

The reasoning behind the usual Chart F3 ordering of :
: Burn-in -> sample High & Low Temps Electrical -> 100% Room Temp Electrical (in 3001 etc.)
is based on the following considerations:
a) To ensure all parts delivered to customers have had the same final inspection/test (i.e. electrical & visual) (the 5 sample components are deliverable as flight and they are not serialised)

b) To ensure that the 5 components that have been subjected to the High & Low Temps Electrical have not been degraded/damaged (due to more risk applying to testing at High & Low Temps)

c) If any failure does occur during the sample High & Low Temps Electrical, a 100% High & Low Temps Electrical may then be performed on the lot which would be, as a result, the final electrical test on the complete lot prior to delivery; again risking delivering degraded/damaged components.

Attachments:

escc3009iss6_draft_a_in_review.docx, escc3012iss6_draft_a_in_review.docx

Modifications:

N/A

Approval signature:



Date signed:

2026-01-28