	ESC	C	DC	DCUMENT	CHANGE REQUEST		
DCR number	1378	Changes required for:	Gen	eral	Originator: Aissa Nehdi		
Date: 2020/10	)/20	Date sent: 2020/09/17	7		Organisation: STMicroelectronics		
Status: IMPLEMENTED							
Title:	Transistors, Low Power, NPN, based on type 2ST15300						
Number:	5201/020	Issue:	1				
Other documen	ts affected:	·		-			
Page:							
13							
Paragraph:							
2.5 PARAMETE	ER DRIFT VALUES						
Original wording	g:						
V(BR)CBO = 30	00V min						
Icbo : limit 1 μ/	 Icbo : limit 1 μA max						
Iebo : limit 200nA max							
Proposed wording:							
V(BR)CBO = 240V min							
 Icbo : limit 10 μA max							
lebo : limit 50μA max							
Justification:	Justification:						
Alignment to device characterization or performance							

	<b>ESCC</b> DOCUMENT CHANGE REQUES						
DCR number	1378	Changes required for:	Gen	eral	Originator: Aissa Nehdi		
Date: 2020/10	/20	Date sent: 2020/09/1	7		Organisation: STMicroelectronics		
Status: IMPLE	MENTED						
Title:	Transistors, Low Power, NPN, based on type 2ST15300						
Number:	5201/020 Issue:			1			
Other documen	ner documents affected:						
Page:							
9							
Paragraph:							
2.4.1 Room Ter	mperature Electrical	Measurements					
Original wording	<b>j</b> :						
ICEO= 500nA r							
	0v limit 500nA max	-					
	√ limit 100nA max						
Cobo 100KHz< F < 1 100 pF Max	MHz						
Ton / Toff Ic = 1A, Vcc=6. Vbb=-8V Ib1=Ib2=100Ma Ton=0.2µs Max Toff= 3µs	I						
Proposed wording:							
ICEO removed from the table							
	ICBO : VCE=300v limit 10μA max						
IEBO : VEB =6 <sup>v</sup>	√ limit 50µA max						
Cobo F =1MHz 120 pF max							

	ESC	C	D	DOCUMENT CHANGE REQUEST						
DCR number	1378	Changes req	uired for: Ger	neral	Originator: Aissa Nehdi					
Date: 2020/10	/20	Date sent: 2	020/09/17		Organisation: STMicroelectronics					
Status: IMPLE	Status: IMPLEMENTED									
Ton / Toff Ic = 3A, Vcc=30V Ib1=300mA, Ib2=300mA Ton=0.4µs Max Toff= 3.5µs										
Justification:										
Alignment to de	evice characterization	n or performan	се							
Title:	Transistors, Low Po	ower, NPN, bas	sed on type 2S	T15300						
Number:	5201/020		lssue:	1						
Other documen	ts affected:									
Page:	Page:									
6										
Paragraph:										
1.5 MAXIMUM	RATINGS									
Original wording	g:									
Ptot1 = 2W Ptot2= 54W Rth(j-c) = $2.3^{\circ}$ C										
Proposed wording:										
Ptot1 = 2.2W Ptot2= 40W Rth(j-c) = $4.38^{\circ}$ C										
Justification:										
Alignment to device characterization or performance										

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	ESC	C	D	OCUMENT	CHANGE REQUEST			
DCR number	1378	Changes required	for: Ge	neral	Originator: Aissa Nehdi			
Date: 2020/10	/20	Date sent: 2020/	09/17		Organisation: STMicroelectronics			
Status: IMPLE	MENTED							
Title:	Transistors, Low Power, NPN, based on type 2ST15300							
Number:	5201/020	Issue	):	1				
Other documen	ts affected:							
Page:								
7	7							
Paragraph:								
1.7 FUNCTION	AL DIAGRAM							
Original wording	<b>j</b> :							
PNP diagram								
Proposed wording:								
NPN diagram								
Justification:								
Alignment to device characterization or performance								

	ESC	C	D	OCUMENT	CHANGE REQUEST			
DCR number	1378	Changes re	quired for: Ge	neral	Originator: Aissa Nehdi			
Date: 2020/10	/20	Date sent: 2	2020/09/17		Organisation: STMicroelectronics			
Status: IMPLEMENTED								
Title:	Transistors, Low Power, NPN, based on type 2ST15300							
Number:	5201/020		Issue:	1				
Other documen	ts affected:							
Page:								
11	11							
Paragraph:								
2.5 PARAMETE	ER DRIFT VALUES							
Original wording	<b>j</b> :							
Icbo : limit 500 nA max								
Proposed wording:								
Icbo : limit 10 μA max								
Justification:								
Alignment to device characterization or performance								

	ESC	C	DOCUMENT	CHANGE REQUEST				
DCR number	1378	Changes required for:	General	Originator: Aissa Nehdi				
Date: 2020/10	)/20	Date sent: 2020/09/17		Organisation: STMicroelectronics				
Status: IMPLE	EMENTED							
Title:	Transistors, Low Power, NPN, based on type 2ST15300							
Number:	5201/020	Issue:	1					
Other documen	ts affected:							
Page:								
11	11							
Paragraph:								
2.4.2 High and	Low Temperatures E	Electrical Measurements						
Original wording	Original wording:							
Icbo : Vcb=240v limit 1 μA max								
Proposed wording:								
Icbo : Vcb=300v 100 μA max								
HFE2 removed from the table								
Justification:								
Alignment to device characterization or performance								

ESCC				DOCUMENT CHANGE REQUEST			
DCR number	1378	Changes re	quired for: (	Gene	eral	Originator: Aissa Nehdi	
Date: 2020/10	)/20	Date sent: 2	2020/09/17			Organisation: STMicroelectronics	
Status: IMPLE	EMENTED						
Title:	Transistors, Low Power, NPN, based on typ			2ST	15300		
Number:	5201/020 Issue:			1			
Other documen	ts affected:						
Page:							
10							
Paragraph:							
2.4.1 Room Ter	mperature Electrical	Measurement	S				
Original wording	g:						
Values of resistors							
Value of Vin= 4	 Value of Vin= 45V						
Proposed wordi	ing:						
Values of resist	ors removed						
Values of Vin re	emoved						
Justification:							
Alignment to device characterization or performance							
Attachments:							
escc5201020iss2_draft_a_in_review.docx, modification_page7_&_10(3).docx							
Modifications:							
See attached spec mark-up for full change details.							
The following change details shall also apply:							
Page 9, Para 2.4.1: For Ton & Toff, it is confirmed that VBB = -8V is not changed.							
Page 11, Para 2.6: For ICBO, change max limit to be 10µA (was 500nA)							
Page 13, Para 2.10.2 (not Para							

Approval signature:

Alisand

Date signed:

2020-10-20