



# DOCUMENT CHANGE REQUEST

DCR number	1200	Changes required for:	General	Originator:	Manuel Morales
Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input Exclusive OR Gates, based on type 54HC86

Number:	9201/119	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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Status:	IMPLEMENTED				

Title: HCMOS Triple 3-Input AND Gates, based on type 54HC11

Number: 9201/117 Issue: 5

Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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DCR number	1200	Changes required for:	General	Originator:	Manuel Morales
Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title: HCMOS Triple 3-Input NOR Gates with fully buffered Outputs, based on type 54HC27

Number:	9201/109	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title:	CMOS Quad 2-Input NOR Gates, based on type 4001B		
Number:	9201/041	Issue:	4

Other documents affected:

Page:

12-13

Paragraph:

1.7.5 Notes to Physical Dimensions and Terminal Identification

Original wording:

New note about lid connection to ground

Proposed wording:

The upper metallic lid is not electrically connected to any pins, nor to the IC die inside the package. Connecting unused pins or metal lid to ground or to the power supply does not affect the electrical characteristics.

Justification:

Lack of information at detailed specification level about the configuration of the lid (floating or ground connection). This information is clearly stated in manufacturer datasheets. The DCr shall apply to several other ESCC specs for similar components



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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input NOR Gates,based on type 54HC02

Number:	9201/113	Issue:	7
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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DCR number	1200	Changes required for:	General	Originator:	Manuel Morales
Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input NAND Gates, based on type 54HC00

Number:	9201/105	Issue:	6
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Other documents affected:

9201/106-5, 9201/107-5, 9201/108-5, 9201/109-5, 9201/110-5, 9201/111-5, 9201/113-7, 9201/114-6, 9201/117-5, 9201/118-5, 9201/119-5, 9201/120-5, 9201/123-5

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title:	HCMOS Quad 2-Input NAND Gates with Open Drain Output, based on type 54HC03				
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Number:	9201/114	Issue:	6
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Quad 2-Input NAND Gate with Schmitt-Trigger
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Number:	9201/120	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:





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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title: HCMOS Qud 2-input OR Gates, based on type 54HC32

Number:	9201/111	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Quad 2-Input and Gate with Fully Buffered Outputs,
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Number:	9201/106	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:



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Date:	2024/02/29	Date sent:	2018/10/29	Organisation:	Alter Technology TÜV Nord
Status:	IMPLEMENTED				

Title: HCMOS Triple 3-Input NAND Gates, based on type 54HC10

Number:	9201/107	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

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Justification:

Attachments:

54hcxx\_datasheets.pdf, improvement\_of\_escs\_specs\_stm\_st\_comments\_aug27th(2).doc, hcc4xxx\_datasheets.pdf, null

Modifications:

Replace the original DCR wording with the following:

1. In "Functional Diagram", for all packages, add a new note: "The package lid for all packages is not connected to any terminal".

2. In "Pin Assignment" for any unused terminals for any package, add the following: "Unconnected terminal".

Approval signature:

Date signed:

2024-02-29