	ES(<u>.</u>	DC	DCUMENT	CHANGE REQUEST				
DCR number	126	Changes re	quired for: Gen	eral	Originator: S Thacker				
Date: 2004/06	e: 2004/06/15 Date sent: 2004/06/15				Organisation: ESA/ESTEC				
Status: IMPLE	MENTED								
Title:	CMOS Analogue Multiplexer/Demultiplexer, based on type 4051B								
Number:	9202/047 Issue: 2								
Other documents affected:									
Page:									
	m Temperature Ele es to Electrical Mea			7, 18					
Paragraph:									
	m Temperature Ele es to Electrical Mea			7, 18					
Original wording	j :								
Proposed wordi	ng:								
tPLH2 & tPHL2 amended as per attached mark-up sheets. Other timing parameters renamed as per mark-up sheets									
Justification:									
tPLH2 & tPHL2	were incorrctly sp	ecified							
Attachments:									
DCR_9202047_markup.pdf, null									
Modifications:									
N/A									
Approval signature:									
Jul Kalo									
Date signed:									
2004-06-15									

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MARK-WP FOR DCR. 5.1 15.6.2004



Pages 1 to 28

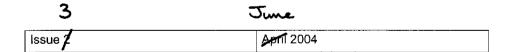
INTEGRATED CIRCUITS, SILICON MONOLITHIC, CMOS

ANALOGUE MULTIPLEXER/DEMULTIPLEXER

(SINGLE 8-CHANNEL)

BASED ON TYPE 4051B

ESCC Detail Specification No. 9202/047





Document Custodian: European Space Agency - see https://escies.org



ISSUE 73

	Characteristics	Symbols MIL-STD-883		Test Conditions	Limits		Units
			Test Method	Note 1	Min	Max	
	Input Clamp Voltage 2, to V _{DD} , Control Inputs	V _{IC2}	-	V_{IN} (Under Test)=6V R=30k Ω , V_{SS} =Open All Other Pins Open Note 6	3		V
	Input Capacitance, Control Inputs	C _{IN}	3012	V _{IN} (Not Under Test)=0V V _{DD} = V _{SS} =V _{EE} =0V f = 100 kHz to 1 MHz Note 7	-	7.5	pF
	Channel Capacitance, CHn	С _{СН}	3012	V _{IN} (Not Under Test)=0V V _{DD} = V _{SS} =V _{EE} =0V f = 100 kHz to 1 MHz Note 7	-	7.5	pF
	Channel Capacitance, COM	С _{СОМ}	3012	V _{IN} (Not Under Test)=0V V _{DD} = V _{SS} =V _{EE} =0V f = 100 kHz to 1 MHz Note 7	-	7.5	pF
	Propagation Delay Low to High X COM to CH0	^t рцн X	3003	$\label{eq:VIN} \begin{array}{l} V_{IN}(COM) = Pulse\\ Generator\\ V_{IN} \ (Remaining\\ Inputs) = Truth \ Table\\ V_{IL} = 0V, \ V_{IH} = 5V,\\ R_L = 200k\Omega\\ V_{DD} = 5V, \ V_{SS} = V_{EE} = 0V\\ Note \ 8 \end{array}$	-	40	ns
	Propagation Delay High to Low X COM to CH0	tphl¥	3003	$\label{eq:VIN} \begin{array}{l} V_{IN}(COM) = \text{Pulse} \\ \text{Generator} \\ V_{IN} \ (\text{Remaining} \\ \text{Inputs}) = \text{Truth Table} \\ V_{IL} = 0V, \ V_{IH} = 5V, \\ R_L = 200 \text{k}\Omega \\ V_{DD} = 5V, \ V_{SS} = V_{EE} = 0V \\ \text{Note 8} \end{array}$	-	40	ns
Output Enable Time High Impedance to High Output 1, A to COM	Propagation Delay Low to High 2, A to COM (Channel ON)	HPEHE EPZHI	3003	$\label{eq:VIN} \begin{array}{l} V_{IN}(A) = Pulse \\ \text{Generator} \\ V_{IN} \ (\text{Remaining} \\ \text{Inputs}) = Truth \ Table \\ V_{IL} = 0V, \ V_{IH} = 5V, \\ V_{IN}(CH) = \begin{array}{l} \Theta V \text{ end} \\ \Theta V \text{ end} \\ SV \text{ end} \\ V_{DD} = 5V, \ V_{SS} = V_{EE} = 0V \\ \text{Note 8} \end{array}$	d Open	670	ns



ISSUE 13

	Characteristics		MIL-STD-883 Test Method	Test Conditions Note 1	Limits		Units
_			Teochicalioa	INOLE	Min	Мах	
Output Disable Time High Output to High Impedance 1, A to COM	PropagationDelay High to Low 2, A to COM (Channel ON)	^{трн<u>г</u>2 Срнз1}	3003	$V_{IN}(A)=Pulse$ Generator $V_{IN} (Remaining$ Inputs)=Truth Table $V_{IL}=0V, V_{IH}=5V,$ $V_{IN}(CH)=0V \text{ and } 5V \text{ and } R_L=10 \text{ km} \text{ 300 s}$ $V_{DD}=5V, V_{SS}=V_{EE}=0V$ Note 8	lOpen	670	ns
	Output Enable Time High Impedance to High Output 2 , INH to COM	^t рzн 2	3003	$\label{eq:VIN} \begin{array}{l} V_{IN}(INH) = Pulse\\ Generator\\ V_{IN} (Remaining\\ Inputs) = Truth Table\\ V_{IL} = 0V, \ V_{IH} = 5V,\\ V_{IN}(CH) = 5V, \ R_L = 10k\Omega\\ V_{DD} = 5V, \ V_{SS} = V_{EE} = 0V\\ Note \ 8 \end{array}$	-	400	ns
	Output Disable Time High Output to High Impedance. 2., INH to COM	^t рнz 2	3003	$\label{eq:VIN} \begin{array}{l} V_{\rm IN}({\rm INH}) = {\rm Pulse} \\ {\rm Generator} \\ V_{\rm IN} \ ({\rm Remaining} \\ {\rm Inputs}) = {\rm Truth \ Table} \\ V_{\rm IL} = 0V, \ V_{\rm IH} = 5V, \\ V_{\rm IN}({\rm CH}) = 5V, \ R_{\rm L} = 300\Omega \\ V_{\rm DD} = 5V, \ V_{\rm SS} = V_{\rm EE} = 0V \\ {\rm Note \ 8} \end{array}$	-	400	ns

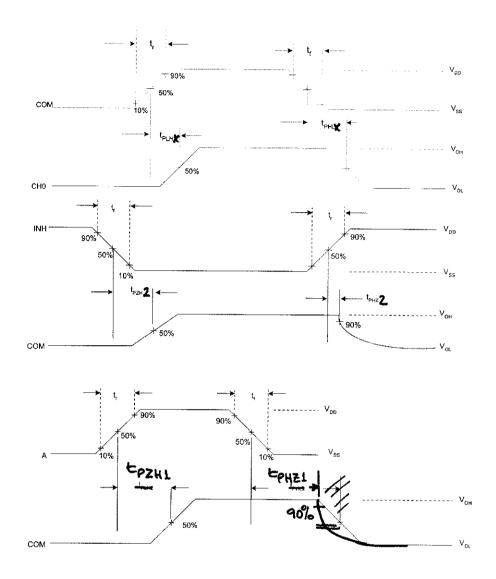
2.3.2 High and Low Temperatures Electrical Measurements

The measurements shall be performed at $T_{amb}\text{=+125}$ (+0 -5) ^{o}C and $T_{amb}\text{=-}$ 55(+5-0) $^{o}\text{C}.$

Characteristics	Symbols	MIL-STD-883	Test Conditions	¢.	Ein	Units	
	Test Method		Note 1		Min	Max	
Functional Test 1	-	3014	$\label{eq:VerifyTruthTable} \hline VerifyTruthTable \\ V_{IL}=0V, V_{IH}=3V \\ V_{DD}=3V, \\ V_{SS}=V_{EE}=0V \\ Note 2 \\ \hline \end{array}$		-	-	-
Functional Test 2	-	3014	Verify Truth Table $V_{IL}=0V, V_{IH}=15V$ $V_{DD}=15V,$ $V_{SS}=V_{EE}=0V$ Note 2			_	-
Quiescent Current	l _{DD}	3005	$V_{IL}=0V, V_{IH}=15V$ $V_{DD}=15V,$ $V_{SS}=V_{EE}=0V$ Note 3 $T_{amb}=+125^{o}C$ $T_{amb}=-55^{o}C$		-	15 0.5	μΑ



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2.4 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.