



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 4 to 16 Line Decoder/Latch, based on type 54HC4514

Number:	9205/019	Issue:	5
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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Dual 4-Bit Negative-Edge-Triggered Binary Counters, based on type 54HC393

Number:	9204/074	Issue:	6
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Other documents affected:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS 8 to 1 Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54HC251				
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Number:	9408/048	Issue:	5
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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Hex Schmitt Trigger Inverters, based on type 54HC14

Number:	9409/007	Issue:	5
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Other documents affected:

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Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS 8-Bit Piso Shift Register with Fully Buffered				
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Number:	9306/042	Issue:	5
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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Synchronous 4-Bit Binary Counter, with direct Clear based on type 54HC161

Number:	9204/059	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Asynchronous Negative-Edge-Triggered 14-Bit
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Number:	9204/076	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS 8 Bit Sipo Shift Latch Registers with 3-State Outputs, based on type 54HC4094				
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Number:	9306/050	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Dual 2-Line to 4-Line Decoders/Demultiplexers with Inverted Outputs, based on type				
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Number:	9205/017	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Synchronous 4-Bit Up/Down Binary Counters (Dual Clock with Clear), based on type				
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Number:	9204/065	Issue:	5		
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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Buffers with 3-State Outputs, based on type 54HC541

Number:	9401/047	Issue:	4
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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit Sipo Shift Register, based on type 54HC164

Number:	9306/041	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 8-Line to 3-Line Priority Encoders, based on type 54HC148

Number:	9410/017	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Line to 1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type

Number:	9408/047	Issue:	5
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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input Exclusive OR Gates, based on type 54HC86

Number:	9201/119	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 9-Bit Odd/Even Parity Generators/Checkers based on type 54HC280

Number:	9208/003	Issue:	5
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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Synchronous Presettable 4-Bit Decade Counter with Direct Clear, based on type 54HC160				
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Number:	9204/062	Issue:	5
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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 3-Line to 8-Line Decoders/Demultiplexers with Address Latches, based on type 54HC237

Number:	9205/021	Issue:	5
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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Hex Inverter with Fully Buffered Outputs, based on
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Number:	9401/033	Issue:	5
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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS 3 to 8 Line Decoder/Demultiplexer with Fully

Number: 9408/046 Issue: 5

Other documents affected:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Hex D-Type Edge-Triggered Flip-Flops with Clear, based on type 54HC174

Number:	9306/052	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Octal Bus Buffer with 3-State Outputs, based on

Number:	9401/048	Issue:	6
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Other documents affected:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Asynchronous Negative Edge Triggered 12-Bit				
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Number:	9204/069	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 4-to-6 Line Decoders/Demultiplexers with Inverted Outputs, based on type 54HC154

Number:	9205/023	Issue:	5
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Other documents affected:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54HC373

Number:	9203/059	Issue:	5
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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit Piso Shift Register, with fully buffered outputs, based on type 54HC166

Number:	9306/043	Issue:	5
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Other documents affected:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Synchronous 4-Bit Up/Down Binary Counter, based on type 54HC191

Number:	9204/066	Issue:	6
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Dual J-K Positive-Edge-Triggered Flip-Flop with

Number:	9306/048	Issue:	5
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Other documents affected:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Triple 3-Input AND Gates, based on type 54HC11				
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Number:	9201/117	Issue:	5
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Other documents affected:

9201/118-5, 9201/119-5, 9201/120-5, 9201/123-5, 9202/075-5, 9202/075-5, 9203/050-5, 9203/052-5, 9203/053-5, 9203/054-6, 9203/059-5, 9203/060-5, 9203/064-5, 9203/070-5, 9203/073-5, 9204/059-5, 9204/062-5, 9204/065-5, 9204/066-6, 9204/069-5, 9204/070-5, 9204/071-5, 9204/074-6, 9204/076-5, 9205/013-5, 9205/017-5, 9205/019-5, 9205/021-5, 9205/023-5, 9208/003-5, 9209/004-5, 9209/005-5, 9306/041-5, 9306/042-5, 9306/043-5, 9306/047-5, 9306/048-5, 9306/050-5, 9306/051-6, 9306/052-5, 9306/054-5, 9401/033-5, 9401/034-5, 9401/037-6, 9401/038-6, 9401/039-5, 9401/044-5, 9401/047-4, 9401/048-6, 9401/049-5, 9402/009-5, 9405/013-6, 9405/013-6, 9408/038-5, 9408/046-5, 9408/047-5, 9408/048-5, 9408/052-5, 9408/054-5, 9408/057-5, 9408/059-5, 9408/064-5, 9408/065-5, 9409/007-5, 9410/017-5

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Analogue Multiplexer/Demultiplexer (Triple 2-Channel),based on type 54HC4053

Number:	9408/065	Issue:	5
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Other documents affected:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Transceivers with 3-State Outputs, based on type 54HC245

Number:	9405/013	Issue:	6
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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Analogue Multiplexer/Demultiplexer, based on type 54HC4051

Number:	9408/064	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Line to 1-Line Data Selectors/Multiplexers, based on type 54HC157

Number:	9408/057	Issue:	5
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Other documents affected:

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Original wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Quad Bilateral Switch, based on type 54HC4066

Number:	9408/052	Issue:	5
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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Octal Bus Buffer with Inverted 3-State Outputs,
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Number:	9401/049	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit Addressable Latches, based on type 54HC259

Number:	9203/073	Issue:	5
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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Quad Bus Buffers with 3 State Outputs, based on type 54HC125

Number: 9401/039 Issue: 5

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS 4-Bit Magnitude Comparators, based on types 54HC85 and 54HC85A

Number:	9209/004	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Dual D-Type Flip-Flop with Preset and Clear, based on type 54HCT74
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Number:	9203/070	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS 8-Bit Shift Registers with 3 State Output Registers, based on type 54HC595				
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Number:	9306/051	Issue:	6
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Octal Bus Transceivers with 3-State Outputs, based on type 54HC245				
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Number:	9405/013	Issue:	6
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS 8-Bit Identity Comparator with Fully Buffered
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Number:	9209/005	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS 8-to-1-Line Data Selectors/Multiplexers with fully buffered outputs, based on type 54HC151				
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Number:	9408/054	Issue:	5
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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Quad 2-Input NAND Gate with Schmitt-Trigger
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Number:	9201/120	Issue:	5
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Original wording:

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Proposed wording:

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DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Hex Buffers/Converters, based on type 54HC4050

Number: 9401/038 Issue: 6

Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	HCMOS Dual 4-Line to 1-Line Data Selectors/Multiplexers, based on type 54HC153				
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Number:	9408/038	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Hex Buffers/Converters with Inverted Inputs, based on type 54HC4049

Number:	9401/037	Issue:	6
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Dual D-Type Flip-Flop with Preset and Clear and

Number: 9203/050 Issue: 5

Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Quad D-Type Edge-Triggered Flip Flops WITH Clear, based on type 54HC175

Number:	9203/052	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS 8-Bit Latch/Shift Register with Fully Buffered
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Number:	9306/054	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54HC574

Number:	9203/054	Issue:	6
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Buffers with 3-State Outputs, based on type 54HCT244

Number:	9402/009	Issue:	5
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Other documents affected:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Asynchronous Negative-Edge-Triggered 14 Bit Binary Counters,based on type 54HC4020

Number:	9204/070	Issue:	5
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Other documents affected:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Edge-Triggered Flip-Flops with Clear, based on type 54HC273

Number:	9203/053	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Buffers with Inverted 3-State Outputs, based on type 54HC240

Number:	9401/034	Issue:	5
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Other documents affected:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1184	Changes required for:	MRB decision	Originator:	Geraldine Chaumont
Date:	2024/02/29	Date sent:	2018/09/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Edge-Triggered Flip-Flops with 3 State Outputs, based on type 54HC374

Number:	9203/060	Issue:	5
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

Justification:

Attachments:

23rwg_13140618_d.pdf, ctb_rwg23_2018_b.pdf, nccs_2cstm801_54hc597_radiation_behavior_rev3.pdf, items_affected.docx, null

Modifications:

Appendix on TD testing

Note: The total number of documents affected by this DCR is 92 (i.e. all HCMOS Detail Specifications except 9205/016 and 9207/008: see attached spreadsheet for list)

A new deviation shall be added to Appendix A in all 92 Detail Specifications:

ITEMS AFFECTED:

Deviations from the Generic Specification: Deviation from Product Control - Chart F2

DESCRIPTION OF DEVIATIONS:

Total Dose Radiation Testing: The following deviation from the procedures for qualification and procurement lot acceptance in ESCC Basic Specification No. 22900 shall apply:

The radiation exposure and test sequence requirements including radiation levels, time intervals for measurement, and the

flow chart for qualification and lot acceptance testing, may be replaced by the requirements of ST radiation test procedure 0043082.

Approval signature:

A handwritten signature in black ink, appearing to read "Stephen R. ...".

Date signed:

2024-02-29