



## DOCUMENT CHANGE REQUEST

DCR number 79

Changes required for: N/A

Originator: Steve Thacker

Date: 2003/12/11

Date sent: 2003/12/11

Organisation: -ESTEC

Status: IMPLEMENTED

Title: CMOS Quad 2-Input NAND Gate, based on type 4011B

Number: 9201/043

Issue: 1

Other documents affected:

Page:

as detailed

Paragraph:

as detailed


Original wording:

Proposed wording:

Total reformat of this detail specification (the first in the series of 97 for the CMOS 400B Series) as part of the ongoing conversion to the ESCC format. See continuation sheet for summary of changes and attached draft Issue 2 of the specification.

Justification:

1. Part of the conversion of coversheeted ESA/SCC specification to the ESCC format.
2. To make format and presentation consistent with the 54HCMOS series of specifications.
3. To incorporate agreed deviations for STm in appendedix A which are considered technically acceptable based on the ESCC approved PID for this qualified series.
4. Update manufacturers current products availability.

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2003-12-11