



DOCUMENT CHANGE REQUEST

DCR number 876 Changes required for: General

Originator: Steve Jeffery

Date: 2014/10/09

Date sent: 2014/08/21

Organisation: ESCC Executive

Status: IMPLEMENTED

Title: CMOS Dual Complementary Pair plus Inverter having unbuffered Outputs, based on type 4007UB

Number: 9202/038

Issue: 3

Other documents affected:

9202/041-3, 9202/042-3, 9202/046-3, 9202/047-4, 9202/048-3, 9202/049-3, 9202/051-3, 9202/060-3, 9202/065-3, 9203/023-3, 9203/038-3, 9204/013-3, 9204/020-4, 9204/021-3, 9204/022-3, 9204/023-3, 9204/024-3, 9204/025-3, 9204/026-3, 9204/028-3, 9204/036-3, 9204/041-3, 9205/011-3, 9206/003-3, 9207/003-3, 9207/007-3, 9208/002-3, 9209/001-3, 9301/009-3, 9306/013-3, 9306/014-3, 9306/015-3

Page:

Specs fully retyped using alternate publishing software (was Framemaker; is now WORD2010)

Paragraph:

All

Original wording:


See each original spec (editorial changes only)

Proposed wording:

Only editorial changes to implement the alternate publishing software (WORD2010)

Justification:

Replacement of obsolete software

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2014-10-09