



## DOCUMENT CHANGE REQUEST

DCR number	873	Changes required for:	General	Originator:	Steve Jeffery
Date:	2014/10/06	Date sent:	2014/07/08	Organisation:	ESCC Executive
Status:	IMPLEMENTED				

Title: CMOS Dual J-K Master Slave Flip-Flop, based on type 4027B

Number: 9203/022 Issue: 3

Other documents affected:

Page:

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Paragraph:

1.9

Original wording:

For FP, DIL and SO, Pin 16 is "-"

Proposed wording:

For FP, DIL and SO, Pin 16 shall be "VDD"

Justification:

A necessary editorial correction, this error was recently notified to ESCIES by P. Putzer of Kayser-Threde.

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2014-10-06