



DOCUMENT CHANGE REQUEST

DCR number 551 Changes required for: General

Originator: S Jeffery

Date: 2009/10/23

Date sent: 2009/10/23

Organisation: ESA/ESTEC

Status: IMPLEMENTED

Title: HCMOS Quad 2-Input NAND Gates, based on type 54HC00

Number: 9201/105

Issue: 3

Other documents affected:

9201/106-2, 9201/107-2, 9201/108-2, 9201/109-2, 9201/110-2, 9201/111-2, 9201/113-4, 9201/114-3, 9201/117-2, 9201/118-2, 9201/119-2, 9201/120-2, 9201/121-3, 9201/122-2, 9201/123-2, 9201/124-2, 9201/129-2, 9201/130-2, 9201/132-2, 9201/133-2, 9202/072-2, 9202/075-2, 9203/050-2, 9203/051-3, 9203/052-1, 9203/053-2, 9203/054-3, 9203/059-2, 9203/060-2, 9203/061-2, 9203/064-2, 9203/065-2, 9203/066-2, 9203/070-2, 9203/071-2, 9203/072-2, 9203/073-2, 9204/059-2, 9204/062-2, 9204/065-2, 9204/066-3, 9204/068-2, 9204/069-2, 9204/070-2, 9204/071-2, 9204/072-2, 9204/073-2, 9204/074-3, 9204/076-2, 9204/077-2, 9204/078-2, 9204/079-2, 9204/080-2, 9205/013-2, 9205/016-3, 9205/017-2, 9205/019-2, 9205/020-2, 9205/021-2, 9205/022-2, 9205/023-2, 9207/006-2, 9207/008-2, 9208/003-2, 9209/004-2, 9209/005-2, 9306/041-2, 9306/042-2, 9306/043-2, 9306/047-2, 9306/048-2, 9306/050-2, 9306/051-3, 9306/052-2, 9306/053-2, 9306/054-2, 9306/055-3, 9401/033-2, 9401/034-2, 9401/035-2, 9401/037-2, 9401/038-2, 9401/039-2, 9401/044-2, 9401/045-2, 9401/046-2, 9401/047-2, 9401/048-3, 9401/049-2, 9401/052-3, 9401/054-2, 9401/055-2, 9402/009-2, 9405/011-2, 9405/012-2, 9405/013-3, 9405/014-2, 9405/016-3, 9408/038-2, 9408/046-2, 9408/047-2, 9408/048-2, 9408/052-2, 9408/054-2, 9408/057-2, 9408/058-2, 9408/059-2, 9408/064-2, 9408/065-2, 9409/007-2, 9410/016-2, 9410/017-2

Page:

Various editorial changes throughout each spec. See also DCR 245 (approved 7th April 2006) and DCR 447 (approved 16th December 2008). It is proposed that once this DCR has been approved, DCRs 245 (where necessary) and 447 are introduced concurrently.

Paragraph:

Various editorial changes throughout each spec. See also DCR 245 (approved 7th April 2006) and DCR 447 (approved 16th December 2008). It is proposed that once this DCR has been approved, DCRs 245 (where necessary) and 447 are introduced concurrently.

Original wording:

Proposed wording:

Various different editorial changes in addition to the changes described in DCR 245. Examples of these changes are shown below.

- 1) Notes to Physical Dimensions and Terminal Identification, Note 6: delete "for flat, dual-in-line and small outline packages".
- 2) Room Temperature Electrical Measurements, Quiescent Current: blank row to be removed (only affects certain specs).
- 3) Room Temperature Electrical Measurements: add "VIL=0V, VIH=4.5V" to all ac parameter test conditions.
- 4) Paras. 2.3.3, 2.4, 2.5 and 2.9.2: delete either "on all gates" or "on all buffers" as applicable.



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5) Para. 2.3.3 Note 5: add "and output load resistance $RL = 1k\Omega \pm 5\%$." (only affects certain specs).

Justification:

All the changes proposed are considered editorial only; in addition to those defined in DCR 245, they are intended to improve the appearance, clarity and consistency of the 54HCMOS series of ESCC Detail Specifications (ref. DCR 485, applicable to the CMOS4000B series of ESCC Detail Specifications).

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2009-10-23