

## DOCUMENT CHANGE REQUEST

542 DCR number Changes required for: General Originator: Steve Thacker - ESCC Date: 2009/09/09 Organisation: ESA/ESTEC Date sent: 2009/09/09 Status: IMPLEMENTED Title: Generic Specification for Integrated Circuits Monolithic Number: 9000 5 Issue: Other documents affected: Page: Page 27, Chart F3 Paragraph: Page 27, Chart F3 Original wording: Proposed wording: Reinstate in Chart F3 the box for: Para. 8.5 - High Temperature Stabilisation Bake (immediately above the Temperature Cycling box) Justification: The Box was inadvertently removed during the previous update (ref. DCR400) due to a document processing error. Attachments: N/A Modifications: N/A Approval signature: Date signed: 2009-09-09