



DOCUMENT CHANGE REQUEST

DCR number 418 Changes required for: N/A

Date: 2008/07/04

Date sent: 2008/07/04

Originator: andre Uguen

Organisation: CNES

Status: IMPLEMENTED

Title: CMOS Hex Buffer/Converter Inverting, based on type 4049UB

Number: 9202/045

Issue: 3

Other documents affected:

9202/046-2

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Paragraph 1.5 Maximum ratings page 6

Paragraph:

Paragraph 1.5 Maximum ratings page 6

Original wording:

Proposed wording:

Characteristic: Input voltage

Symbol: V_{in}

Maximum rating: -0.5v to 18.5v (instead of -0.5v to $V_{dd}+0.5v$)


Unit: V

Justification:

This proposed wording was the one present in the old ESA specifications (or ESCC issue1) of these two devices (HCC4049B & HCC4050B).

We suppose that during the migration to new specification format the modification was performed in order to uniformize all CMOS4000B devices. but it was not on manufacturer demand.

In fact 3 devices have different inputs configuration that allow to have an input signal greater than V_{dd} : HCC4049B, HCC4050B & HCC40109B. Concerning this last device (9407/003) the modification was not implemented during migration. We want now to go back to initial wording.

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2008-07-04