



# DOCUMENT CHANGE REQUEST

DCR number	305	Changes required for:	N/A	Originator:	Andre Uguen
Date:	2006/12/04	Date sent:	2006/12/04	Organisation:	CNES
Status:	IMPLEMENTED				

Title: CMOS Hex Buffer/Converter Inverting, based on type 4049UB

Number:	9202/045	Issue:	2
---------	----------	--------	---

Other documents affected:

Page:

Â§ 2.3.1 page 15  
Â§ 2.3.2 page 17

Paragraph:

Â§ 2.3.1 page 15  
Â§ 2.3.2 page 17

Original wording:

Proposed wording:

for VOL2 Test conditions: "VIL=1V, VIH=4V, IOL=0A, VDD=5V, VSS=0V"  
for VOH2 Test conditions: "VIL=1V, VIH=4V, IOH=0A, VDD=5V, VSS=0V"  
for VOL3 Test conditions: "VIL=2.5V, VIH=12.5V, IOL=0A, VDD=15V, VSS=0V"  
for VOH3 Test conditions: "VIL=2.5V, VIH=12.5V, IOH=0A, VDD=15V, VSS=0V"

Justification:

This device 4049UB is UB (unbuffered) as 4007UB, 4041UB and 4069UB.  
During the transfert from ESA form to ESCC form the test conditions should have been copied from a buffered device. (VIL and VIH are different)

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2006-12-04