



DOCUMENT CHANGE REQUEST

DCR number 1768 Changes required for: MRB decision		Originator: Carole Gagnard	
Date: 2025/11/17 Date sent: 2025/10/02		Organisation: Rakon	
Status: IMPLEMENTED			

Title:	CRYSTAL OSCILLATORS, CLASS 2, 4MHz TO 100MHz, AHCMOS AND AC MOS COMPATIBLE		
Number:	3503/001	Issue:	8

Other documents affected:

Page:

a) 17
b) 18

Paragraph:

a) 2.4
b) 2.5

Original wording:

a) 2.4 Input Current Drift Value +/-5%

b) 2.5 Frequency Accuracy Drift Value +/-8.5 ppm (1)
Frequency-Temperature Stability
For Variants 01 to 04:
For fNom 4MHz to 80MHz: +/- 30ppm
For fNom 80MHz to 100MHz: +/- 40 ppm
For Variant 06:
For fNom 4MHz to 100MHz : +/- 50 ppm

Proposed wording:

a) 2.4 Input Current Drift Value +/-10%

b) 2.5 Frequency Accuracy Drift Value +/-25 ppm (1)
Frequency-Temperature Stability
For Variants 01 to 04:
For fNom 4MHz to 80MHz: +/- 50ppm
For fNom 80MHz to 100MHz: +/- 60 ppm
For Variant 06:
For fNom 4MHz to 100MHz : +/- 70 ppm

Justification:

a) Specified consumption drift after burn-in i.e. 5% is too tight versus reproducibility for some products with real consumption around 5 mA.



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Status: IMPLEMENTED			
Limit of MIL-PRF-55310 para.4.4.5 is 10%			
b) NCCS NC2RAK2409 and NC2RAKC2511: too tight specification			
Attachments:			
N/A			
Modifications:			
N/A			
Approval signature:			
			
Date signed:			
2025-11-17			