ESCC

DOCUMENT CHANGE REQUEST

DCR number 189	Changes required for: N/A	Originator: P. Griffin
Date: 2005/06/28	Date sent: 2005/06/28	Organisation: ESA/ESTEC
Status: IMPLEMENTED		

Title:	CMOS 4-Bit Bi-Directional Universal Shift Register with 3 State Outputs, based on type 40104B			
Number:	9306/040	Issue:	1	

Other documents affected:

Page:

Figure 3(b) - Truth Table Page 13

Paragraph:

Figure 3(b) - Truth Table Page 13

Original wording:

Proposed wording:

In Truth Table for RESET (first line of Truth Table below headings)change CL from X to positive clock pulse, (same as Note 2 to the Truth Table).

Justification:

Correction of error in 9306040 issue 1DCR amended on 29-7

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2005-06-28