DOCUMENT CHANGE REQUEST

DCR number **188**	Changes required for: N/A		Originator: P. Griffin
Date: 2005/06/28	Date sent: 2005/06/28		Organisation: ESA/ESTEC
Status: IMPLEMENTED			

Title:	CMOS 8-Stage Synchronous Static Shift Register, based on type 4014B		
Number:	9306/014	Issue:	1

Other documents affected:

Page:

Figure 3(b) Truth Table, Page 14

Paragraph:

Figure 3(b) Truth Table, Page 14

Original wording:

Proposed wording:

In Serial operation and Parallel operation tables correct Pn to P/S(bar) [Parallel/Serial Control, Pin 9 (DIP, SO, FP packages) and Pin 11 (CCP package)]in column headers

Justification:

Correction of error in 9306014 issue 1

DCR amended on 29-7-2005

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2005-06-28