

DCR number

DCR amended on 29-7-2005

183

DOCUMENT CHANGE REQUEST

Originator: S. Jeffery

Date: 2005/06/06 Date sent: 2005/06/06 Organisation: ESA/ESTEC Status: IMPLEMENTED Title: CMOS Low Power Monostable/Astable Multivibrator, based on type 4047B Number: 9207/003 Issue: 1 Other documents affected: Page: Page 13, Figure 3(b), TRUTH TABLE and Page 15, Figure 3(d), FUNCTIONAL DIAGRAM. NB: after spec conversion, Figure 3(d) will be Para. 1.8 and Figure 3(b) will be Para. 1.10. Paragraph: Page 13, Figure 3(b), TRUTH TABLE and Page 15, Figure 3(d), FUNCTIONAL DIAGRAM. NB: after spec conversion, Figure 3(d) will be Para. 1.8 and Figure 3(b) will be Para. 1.10. Original wording: Proposed wording: Add the following note to the Functional Diagram:-Cext (Astable mode)>=100pF; Cext (Monostable mode)>=1nF. 10kOhms<=Rext<=1MOhm Add the following note to the Truth Table:-For FP, DIP and SO packages, an external resistor (Rext) and an external capacitor (Cext) are required to be connected between pins 2 and 3 and pins 1 and 3 respectively. For the CCP package external components Rext and Cext are required to be connected between pins 4 and 5 and pins 2 and 5 respectively. See Para. 1.8 for values of external components. [note - 'ext' will be subscript] Justification: The proposed notes are required to fully define the external components and hence the device itself. The information in the proposed notes is based on that given in the manufacturer's data sheet.

Changes required for: General



DOCUMENT CHANGE REQUEST

DCR number	183	Changes required for: General	Originator: S. Jeffery
Date: 2005/06/06		Date sent: 2005/06/06	Organisation: ESA/ESTEC
Status: IMPLEMENTED			
Attachments:			
N/A			
Modifications:			
N/A			
Approval signature:			
Jl Karea			
Date signed:			
2005-06-06			