



DOCUMENT CHANGE REQUEST

DCR number 144

Changes required for: N/A

Originator: P. Griffin

Date: 2004/10/25

Date sent: 2004/10/25

Organisation: ESA/ESTEC

Status: IMPLEMENTED

Title: CMOS 14-Stage Ripple-Carry Binary Counter/Divider and Oscillator, based on type 4060B

Number: 9204/052

Issue: 2

Other documents affected:

Page:

Para 1.9 Page 13, Para 1.10 Page 13, Para 2.3.1 Page 15, Para 2.3.2 Page 18, Para 2.3.3 Page 20.

Paragraph:

Para 1.9 Page 13, Para 1.10 Page 13, Para 2.3.1 Page 15, Para 2.3.2 Page 18, Para 2.3.3 Page 20.


Original wording:

Proposed wording:

- 1) Para 1.9 Addition of "(Clock)" to pin function for FP, DIP and SO pins 9 and 10 and CCP pins 11 and 12.
- 2) Para 1.10 Counter Truth table corrected, Oscillator Truth table added.
- 3) Para 2.3.1 High level and Low Level Output Currents IOLA and IOHA characteristic description amended to "CLKO and barCLKO" IOL2 Unit corrected to "mA".
- 4) Para 2.3.2 High level and Low Level Output Currents IOLA and IOHA characteristic description amended to "CLKO and barCLKO"
- 5) Para 2.3.3 Note 9 "pluse" corrected to "pulse"

Justification:

- 1) Clarification of pin function.
- 2) Corrected Counter Truth Table. Truth Table of Oscillator included.
- 3) Editorial changes to align with other characteristics. Correction of unit for IOL2 test.
- 4) Editorial changes to align with other characteristics.
- 5) Correction of typing error.

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2004-10-25