



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Dual 4-Bit Negative-Edge-Triggered Binary Counters, based on type 54HC393

Number:	9204/074	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Presettable Divide-by-N Counter, based on type 4018B

Number: 9204/021 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS 3 to 8 Line Decoder/Demultiplexer with Fully

Number: 9408/046 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Quad 2-Input NAND Gate with Schmitt-Trigger

Number: 9201/120 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad Bus Buffers with 3 State Outputs, based on type 54HC125

Number:	9401/039	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Triple 3-Input OR Gates, based on type 4075B

Number:	9201/065	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Synchronous Presettable 4-Bit Decade Counter with Direct Clear, based on type 54HC160

Number:	9204/062	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Buffers with Inverted 3-State Outputs, based on type 54HC240

Number: 9401/034 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-input OR Gates, based on type 54HC32

Number:	9201/111	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Hex Non-Inverting Buffers with 3-State Outputs, based on type 4503B

Number:	9401/030	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 2-Input NAND Gate, based on type 4011B

Number:	9201/043	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Edge-Triggered Flip-Flops with Clear, based on type 54HC273

Number:	9203/053	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual 1-of-4 Decoder/Demultiplexers (Outputs Low on Select), based on type 4556B

Number:	9408/025	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Asynchronous Negative-Edge-Triggered 14 Bit Binary Counters,based on type 54HC4020

Number:	9204/070	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual 4-Input OR Gates, based on type 4072B

Number: 9201/082 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS BCD-to-Decimal OR Binary-to-Octal Decoder, based on type 4028B

Number:	9205/010	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Asynchronous Negative-Edge-Triggered 14-Bit

Number: 9204/076 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Analogue Multiplexer/Demultiplexer, based on type 4067B

Number: 9408/009 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit Shift Registers with 3 State Output Registers, based on type 54HC595

Number:	9306/051	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Synchronous 4-Bit Binary Counter, with direct Clear based on type 54HC161

Number: 9204/059 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad Bilateral Switch, based on type 54HC4066

Number: 9408/052 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Triple 3-Input AND Gates, based on type 54HC11

Number:	9201/117	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Low Power Monostable/Astable Multivibrator, based on type 4047B

Number: 9207/003 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit PISO Shift Register, with fully buffered outputs, based on type 54HC166

Number: 9306/043 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 2-Input Exclusive OR Gates, based on type 4030B

Number: 9201/047 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Hex Buffer/Converter Inverting, based on type 4049UB

Number: 9202/045 Issue: 6

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Presettable Binary Up/Down Counter With Fully Buffered Outputs, based on type 4516B

Number:	9204/045	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Dual 2-Line to 4-Line Decoders/Demultiplexers with Inverted Outputs, based on type

Number: 9205/017 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Hex Inverter, based on type 4069UB

Number: 9401/010 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Ripple-Carry Binary Counter/Divider, based on type 4020B

Number: 9204/022 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Dual J-K Positive-Edge-Triggered Flip-Flop with

Number: 9306/048 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54HC373

Number:	9203/059	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Line to 1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type

Number:	9408/047	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual 4-Stage Static Shift Register with Serial Input/Parallel Input, based on type 4015B

Number:	9306/015	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 8-Input NAND/AND Gate, based on type 4068B

Number:	9201/061	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Octal Counter/Divider, based on type 4022B

Number: 9204/023 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 2-Input AND Gate, based on type 4081B

Number:	9201/052	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Presettable Up/Down Counter Binary OR BCD Decade, based on type 4029B

Number: 9204/025 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Hex 'D'-Type Flip-Flop, based on type 40174B

Number:	9203/038	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Octal Bus Buffer with 3-State Outputs, based on

Number:	9401/048	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input NAND Gates, based on type 54HC00

Number:	9201/105	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit Addressable Latches, based on type 54HC259

Number: 9203/073 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad D-Type Edge-Triggered Flip Flops WITH Clear, based on type 54HC175

Number:	9203/052	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Analogue Multiplexer/Demultiplexer, based on type 4052B

Number:	9202/048	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual 'D'-Type Flip-flop, based on type 4013B

Number:	9203/023	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 8-Stage Synchronous Static Shift Register, based on type 4014B

Number:	9306/014	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title:	HCMOS Dual D-Type Flip-Flop with Preset and Clear, based on type 54HCT74				
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Number:	9203/070	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 9-Bit Odd/Even Parity Generators/Checkers based on type 54HC280

Number:	9208/003	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Analogue Multiplexer/Demultiplexer (Triple 2-Channel), based on type 4053B

Number:	9202/049	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Dual Positive or Negative Edge Schmitt-Retriggerable Monostable Multivibrators with

Number: 9207/006 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual 1-of-4 Decoder/Demultiplexer, based on type 4555B

Number:	9408/011	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual J-K Master Slave Flip-Flop, based on type 4027B

Number: 9203/022 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54HC573

Number: 9202/072 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input NOR Gates, based on type 54HC02

Number: 9201/113 Issue: 7

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Buffers with 3-State Outputs, based on type 54HC541

Number:	9401/047	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Analogue Multiplexer/Demultiplexer (Triple 2-Channel),based on type 54HC4053

Number:	9408/065	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Hex D-Type Edge-Triggered Flip-Flops with Clear, based on type 54HC174

Number:	9306/052	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Hex Buffers/Converters with Inverted Inputs, based on type 54HC4049

Number: 9401/037 Issue: 6

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad Low to High 3-State Voltage Level Shifter, based on type 40109B

Number:	9407/003	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title:	CMOS 8-Stage Shift and Store Bus Register with Synchronous Serial Outputs and 3-State Parallel				
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Number:	9306/026	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 14-Stage Ripple-Carry Binary Counter/Divider and Oscillator, based on type 4060B

Number:	9204/052	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Edge-Triggered Flip-Flops with 3 State Outputs, based on type 54HC374

Number:	9203/060	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Triple 3-Input AND Gate, based on type 4073B

Number: 9201/064 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 3-Line to 8-Line Decoders/Demultiplexers with Address Latches, based on type 54HC237

Number: 9205/021 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 2-Input NAND-Gate with Schmitt Trigger Inputs, based on type 4093B

Number:	9409/002	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual 4-Input NOR Gates, based on type 4002B

Number:	9201/042	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 2-Input OR Gates, based on type 4071B

Number:	9201/063	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 2-Input NOR Gates, based on type 4001B

Number: 9201/041 Issue: 5

Other documents affected:

9201/042-5, 9201/043-5, 9201/047-5, 9201/048-5, 9201/052-5, 9201/055-5, 9201/061-5, 9201/063-5, 9201/064-5, 9201/065-5, 9201/082-5, 9201/105-6, 9201/106-5, 9201/107-5, 9201/108-5, 9201/109-5, 9201/110-5, 9201/111-5, 9201/113-7, 9201/114-6, 9201/117-5, 9201/118-5, 9201/119-5, 9201/120-5, 9201/123-5, 9202/039-5, 9202/040-5, 9202/042-5, 9202/043-5, 9202/044-5, 9202/045-6, 9202/046-5, 9202/047-6, 9202/048-5, 9202/049-5, 9202/051-5, 9202/065-5, 9202/072-5, 9202/075-5, 9203/022-5, 9203/023-6, 9203/038-5, 9203/050-5, 9203/052-5, 9203/053-5, 9203/054-6, 9203/059-5, 9203/060-5, 9203/064-5, 9203/070-5, 9203/073-5, 9204/020-6, 9204/021-5, 9204/022-5, 9204/023-5, 9204/024-5, 9204/025-5, 9204/026-5, 9204/028-5, 9204/036-5, 9204/041-5, 9204/045-5, 9204/052-6, 9204/054-5, 9204/059-5, 9204/062-5, 9204/065-5, 9204/066-6, 9204/069-5, 9204/070-5, 9204/071-5, 9204/074-6, 9204/076-5, 9205/010-5, 9205/011-5, 9205/013-5, 9205/017-5, 9205/019-5, 9205/021-5, 9205/023-5, 9206/003-5, 9207/003-5, 9207/006-5, 9207/007-5, 9208/003-5, 9209/001-5, 9209/004-5, 9209/005-5, 9306/014-5, 9306/015-5, 9306/016-5, 9306/022-5, 9306/026-5, 9306/041-5, 9306/042-5, 9306/043-5, 9306/047-5, 9306/048-5, 9306/050-5, 9306/051-6, 9306/052-5, 9306/054-5, 9401/010-5, 9401/013-5, 9401/030-5, 9401/033-5, 9401/034-5, 9401/037-6, 9401/038-6, 9401/039-5, 9401/044-5, 9401/047-5, 9401/048-6, 9401/049-5, 9402/009-5, 9405/013-6, 9405/014-5, 9407/003-6, 9408/005-5, 9408/006-5, 9408/009-5, 9408/011-6, 9408/012-5, 9408/025-5, 9408/038-5, 9408/046-5, 9408/047-5, 9408/048-5, 9408/052-5, 9408/054-5, 9408/057-5, 9408/059-5, 9408/064-5, 9408/065-5, 9409/002-5, 9409/005-4, 9409/007-5, 9410/017-5

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Synchronous 4-Bit Up/Down Binary Counters (Dual Clock with Clear), based on type

Number: 9204/065 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Analogue Multiplexer/Demultiplexer, based on type 54HC4051

Number: 9408/064 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54HC574

Number:	9203/054	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8-Bit Sipo Shift Register, based on type 54HC164

Number: 9306/041 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual Binary Up Counter, based on type 4520B

Number:	9204/028	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Decade Counter/Divider, based on type 4017B

Number: 9204/020 Issue: 6

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS 8-Bit Identity Comparator with Fully Buffered

Number: 9209/005 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Hex Schmitt Trigger Inverters, based on type 54HC14

Number: 9409/007 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS 8-Bit Piso Shift Register with Fully Buffered

Number: 9306/042 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Octal Bus Buffer with Inverted 3-State Outputs,

Number: 9401/049 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input Exclusive OR Gates, based on type 54HC86

Number:	9201/119	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual Monostable Multivibrator with Reset, based on type 4538B

Number: 9207/007 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 8-Channel Multiplexer with 3-State Outputs, based on type 4512B

Number: 9408/006 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Dual 4-Line to 1-Line Data Selectors/Multiplexers, based on type 54HC153

Number: 9408/038 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS 8-Bit Latch/Shift Register with Fully Buffered

Number: 9306/054 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Micropower Phase-Locked Loop, based on type 4046B

Number: 9202/044 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 4-Bit Magnitude Comparators, based on types 54HC85 and 54HC85A

Number: 9209/004 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8-Line to 3-Line Priority Encoders, based on type 54HC148

Number: 9410/017 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Buffers with 3-State Outputs, based on type 54HCT244

Number: 9402/009 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Analogue Multiplexer/Demultiplexer, based on type 4051B

Number: 9202/047 Issue: 6

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8-to-1-Line Data Selectors/Multiplexers with fully buffered outputs, based on type 54HC151

Number:	9408/054	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Asynchronous Negative Edge Triggered 12-Bit				
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Number:	9204/069	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad Bilateral Switch, based on type 4066B

Number:	9408/005	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad Exclusive NOR Gate, based on type 4077B

Number: 9201/055 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad AND/OR Select Gate, based on type 4019B

Number: 9202/051 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Hex Inverter with Fully Buffered Outputs, based on

Number: 9401/033 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad True/Complement Buffer with Unbuffered Outputs, based on type 4041UB

Number: 9202/040 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Triple 3-Input NOR Gates with fully buffered Outputs, based on type 54HC27

Number:	9201/109	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Transceivers with 3-State Outputs, based on type 54HCT245

Number: 9405/014 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Dual Monostable Multivibrator, based on type 4098B

Number: 9206/003 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 4-Bit Magnitude Comparator, based on type 4063B

Number: 9209/001 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 8-Stage Static Shift Register, based on type 4021B

Number: 9306/016 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Hex Buffer/Converter Non-Inverting, based on type 4050B

Number: 9202/046 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Presetable 8-Bit Synchronous Down Counters, based on type 40103B

Number: 9204/036 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Octal Bus Transceivers with 3-State Outputs, based on type 54HC245

Number: 9405/013 Issue: 6

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad Exclusive OR Gate, based on type 4070B

Number: 9201/048 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Quad 2-Input and Gate with Fully Buffered Outputs,

Number: 9201/106 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Synchronous 4-Bit Up/Down Binary Counter, based on type 54HC191

Number:	9204/066	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Line to 1-Line Data Selectors/Multiplexers, based on type 54HC157

Number: 9408/057 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Ripple Carry Binary Counter/Divider, based on type 4040B

Number:	9204/026	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8 Bit Sipo Shift Latch Registers with 3-State Outputs, based on type 54HC4094

Number: 9306/050 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 4-to-6 Line Decoders/Demultiplexers with Inverted Outputs, based on type 54HC154

Number: 9205/023 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS Quad 3-State NOR R/S Latches, based on type 4043B

Number: 9202/042 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Quad 2-Input NAND Gates with Open Drain Output, based on type 54HC03

Number:	9201/114	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 8 to 1 Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54HC251

Number:	9408/048	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Triple 3-Input NAND Gates, based on type 54HC10

Number: 9201/107 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS 4-Bit Latch/4-to-16 Decoder, based on type 4514B

Number: 9408/012 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: CMOS HEX Schmitt Triggers, based on type 40106B

Number:	9409/005	Issue:	4
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS Hex Buffers/Converters, based on type 54HC4050

Number:	9401/038	Issue:	6
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: HCMOS 4 to 16 Line Decoder/Latch, based on type 54HC4514

Number:	9205/019	Issue:	5
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Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:

Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:



DOCUMENT CHANGE REQUEST

DCR number	1258	Changes required for:	General	Originator:	Steve Thacker
Date:	2024/02/29	Date sent:	2019/03/19	Organisation:	ESCC Executive Secretariat
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, HCMOS Dual D-Type Flip-Flop with Preset and Clear and

Number: 9203/050 Issue: 5

Other documents affected:

Page:

Various

Paragraph:

1.4.2, 1.7 & others as applicable

Original wording:

Per currently published & still supported ESCC specs

Proposed wording:


Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019):

- Var 01 FP with G2 leads
- Var 02 FP with G4 leads
- Var 03 DIP with G2 leads
- Var 04 DIP with G4 leads

Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.:

Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.

Justification:

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2024-02-29