



## DOCUMENT CHANGE REQUEST

DCR number 1189 Changes required for: General

Date: 2019/01/11

Date sent: 2018/11/21

Status: IMPLEMENTED

Originator: Steve Thacker

Organisation: ESCC Executive  
Secretariat

Title: Transistors, Low Power, Complementary NPN/PNP Based on type 2ST3360

Number: 5207/009

Issue: 1

Other documents affected:

Page:

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Paragraph:

2.4.1.2 PNP Transistor Room Temperature Electrical Measurements

Original wording:

Output Capacitance Cobo = 45pF maximum

Proposed wording:

Output Capacitance Cobo = 60pF maximum

Justification:

error identified by supporting Manufacturer ST

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2019-01-11