



DOCUMENT CHANGE REQUEST

DCR number 111 Changes required for: General
Date: 2004/03/22 Date sent: 2004/03/22
Status: IMPLEMENTED

Originator: Paul Roberts
Organisation: -ESTEC

Title: CMOS Quad AND/OR Select Gate, based on type 4019B

Number: 9202/051 Issue: 1

Other documents affected:

Page:

Fig 4(b), page 33. Quiescent current Test Table.
Pattern No. 4.

Paragraph:

Fig 4(b), page 33. Quiescent current Test Table.
Pattern No. 4.

Original wording:

Proposed wording:

Inputs for Pins 9 & 14 are transposed. Correct inputs are:
Pin 9 = 0, Pin 14 = 1.

Justification:

Correction of typing error.

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2004-03-22