



DOCUMENT CHANGE REQUEST

DCR number 101 Changes required for: General
Date: 2004/02/04 Date sent: 2004/02/04
Status: IMPLEMENTED

Originator: Paul Roberts
Organisation: ESA/ESTEC

Title: CMOS 8-Input NOR/OR Gate, based on type 4078B

Number: 9201/062 Issue: 1

Other documents affected:

Page:

Page 20. Table 2. Test Nos. 113 to 120. Input Clamp Voltage Vic1.

Paragraph:

Page 20. Table 2. Test Nos. 113 to 120. Input Clamp Voltage Vic1.

Original wording:

Proposed wording:

Maximum Limit is shown as 2.0 V. Should read -2.0V.

Justification:

Typographic Error.

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2004-02-04