ESCC

DOCUMENT CHANGE REQUEST

DCR number 936	Changes required for: General	Originator: Steve Jeffery
Date: 2020/11/16	Date sent: 2015/05/29	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	Capacitor Filters, C-Type, Electromagnetic Interference Suppression, Surface Mount, based on

Number:	3008/034	Issue:	2

Other documents affected:

9201/040-4, 9201/054-4, 9201/068-4, 9201/132-4, 9201/133-4, 9202/078-3, 9204/067-4, 9204/077-4, 9204/078-4, 9204/079-4, 9204/080-4, 9301/047-3, 9301/048-2, 9301/049-2, 9301/050-2, 9306/034-4, 9306/040-4, 9407/004-2, 9521/002-2

Page:

Specs fully retyped using alternate publishing software (was Framemaker; is now MSWORD2010)

Paragraph:

All

Original wording:

See each original spec (editorial changes only)

Proposed wording:

Only editorial changes to implement the alternate publishing software (MSWORD2010)

Justification:

Replacement of obsolete software

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DCR number **936**	Changes required for: General	Originator: Steve Jeffery
Date: 2020/11/16	Date sent: 2015/05/29	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	IC Silicon Monolithic CMOS STATIC 4K x 9-Bit Parallel First in First Out Memory with Three State

Number:	9301/049	Issue:	2

Other documents affected:

Page:

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Date: 2020/11/16	Date sent: 2015/05/29		Organisation: ESCC Executive
Status: IMPLEMENTED			

Title:	IC Silicon Monolithic CMOS STATIC 16K x 9-Bit Parallel First in First Out Memory with Three State

Number:	9301/048	Issue:	2

Other documents affected:

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DCR number **936**	Changes required for: General	Originator: Steve Jeffery
Date: 2020/11/16	Date sent: 2015/05/29	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	IC Silicon Monolithic, CMOS Static 128k x 8 (131072 x 8 BIT) Asynchronous Random Access

Number:	9301/047	Issue:	3

Other documents affected:

Page:

Specs fully retyped using alternate publishing software (was Framemaker; is now MSWORD2010)

Paragraph:

All

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Justification:

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DOCUMENT CHANGE REQUEST

DCR number 936	Changes required for: General	Originator: Steve Jeffery
Date: 2020/11/16	Date sent: 2015/05/29	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	CMOS 8-Bit Micro-Controller, based on types 80C32E and 80C52E

Number:	9521/002	Issue:	2

Other documents affected:

Page:

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Justification:

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DOCUMENT CHANGE REQUEST

DCR number 936	Changes required for: General	Originator: Steve Jeffery
Date: 2020/11/16	Date sent: 2015/05/29	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title: IC Silicon Monolithic, 10-Bit, 2.2GSPS, Analogue to Digital Converter based on Type AT84AS008

Number:	9407/004	Issue:	2

Other documents affected:

Page:

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Paragraph:

All

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Justification:

Replacement of obsolete software

ESCC	**DOCUMENT CHANGE REQUEST**	

DCR number 936	Changes required for: General	Originator: Steve Jeffery
Date: 2020/11/16	Date sent: 2015/05/29	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	IC Silicon Monolithic CMOS 8K x 16 Dual Port Static Random Access Memory with Three State

Number:	9301/050	Issue:	2

Other documents affected:

Page:

Specs fully retyped using alternate publishing software (was Framemaker; is now MSWORD2010)

Paragraph:

All

Original wording:

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Proposed wording:

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Justification:

Replacement of obsolete software

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2020-11-16