



DOCUMENT CHANGE REQUEST

DCR number 142 Changes required for: General

Date: 2004/10/05

Date sent: 2004/10/05

Originator: Steve Thacker

Organisation: ESA/ESTEC

Status: IMPLEMENTED

Title: CMOS 32/40-Bit IEEE Floating Point Digital Signal Processor, based on Type TSC21020F

Number: 9512/002

Issue:

1

Other documents affected:

Page:

Page 6, Para 1.5 Max Ratings, Thermal Resistance

Paragraph:

Page 6, Para 1.5 Max Ratings, Thermal Resistance


Original wording:

Proposed wording:

Amend Thermal resistance maximum rating $R_{th}(J-C)$ to be $3^{\circ}C/W$ (was $0.3^{\circ}C/W$)

Justification:

The current value for Thermal Resistance is incorrect as confirmed by manufacturer Atmel.
Atmel advise the following justification for the correction to Thermal Resistance ($R_{th}(J-C)$) rating:
Package supplier data giving $0.3^{\circ}C/W$ does not take into account the various interface characteristics (die to die attach and die attach to package base).
Measurements on packages with die, made by Atmel indicate that the true value is $3^{\circ}C/W$.
The Atmel Data sheet will be updated accordingly.

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2004-10-05