MARK-UP FOR DCR396 S. Thaker 17/07/08.



Pages 1 to 12

DIODES, POWER RECTIFIER, HIGH EFFICIENCY, FAST RECOVERY

BASED ON TYPE BYV54-200

ESCC Detail Specification No. 5103/031

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ISSUE \$ 4 (on all pages)

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DOCUMENTATION CHANGE NOTICE

(Refer to https://escies.org for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
287	Specification up issued to incorporate editorial and technical changes per DCR.
396	





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1. GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 5000
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 The ESCC Component Number

The ESCC Component Number shall be constituted as follows:

Example: 510303101

- Detail Specification Reference: 5103031
- Component Type Variant Number: 01 (as required)

1.4.2 <u>Component Type Variants</u>

The component type variants applicable to this specification are as follows:

¥	Nur
(Add various	(
02 to 05 see attached)	e lea

Variant Number	Based on Type	Case	Description	Lead Material and Finish	Weight max g
01	BYV54-200	TO-254	Single diode	H9	10

The lead material and finish shall be in accordance with the requirements of ESCC Basic Specification No. 23500.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Maximum Ratings	Unit	Remarks
Forward Surge Current	I _{FSM}	400	Α	Note 1
Repetitive Peak Reverse Voltage	V _{RRM}	200	٧	Note 2



Para 1.4.2 Component Type Variants

Variant Number	Based on Type	Case	Description	Lead Material and Finish	Weight Max g
01	BYV54-200	TO-254	Single diode	H9	10
02	BYV54-200	TO-254	Single diode	H14	10
03	BYV54-200	TO-254	Single diode	H4	10
04	BYV54-200	TO-254AA	Single diode-Low Ohmic Package	S14	10
05	BYV54-200	TO-254AA	Single diode-Low Ohmic Package	S4	10



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Characteristics	Symbols	Maximum Ratings	Unit	Remarks
Average Output Rectified Current	lo	40	А	50% Duty Cycle Note 3
RMS Forward Current	I _{F(rms)}	60	Α	
Operating Temperature Range (Case Temperature)	T _{op}	-55 to +150	°C	Note 4
Junction Temperature	Tj	+150	°C	
Storage Temperature Range	T _{stg}	-55 to +150	°C	Note 4
Soldering Temperature	T _{sol}	+260	°C	Note 5
Thermal Resistance, Junction to Case	R _{th(j-c)}	1	°C/W	Note 6

NOTES:

1. Sinusoidal pulse of 10ms duration. 3. At Tcase > +90°C, denote linearly to OA at +150°C for Variants 01,02,03

Pulsed, duration 5ms, f = 50Hz.

Pulsed, duration 5ms, f = 50Hz.

At Trace > +990c, derate linearly to 0A at +150°C.

At Trace > +110°C, derate linearly to 0A at +150°C. for Variants 04,05

For Variants with hot solder dip lead finish, all testing performed at Tamb > +125°C shall be carried out in a 100% inert atmosphere.

Duration 10 seconds maximum at a distance of not less than 1.5mm from the device body and the 5. same lead shall not be resoldered until 3 minutes have elapsed.

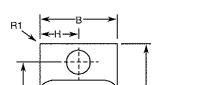
Package mounted on an infinite heatsink. 6.

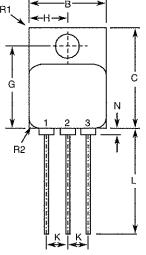
and TO-254AA packages

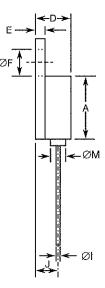
HANDLING PRECAUTIONS

The TO-254/package contain Beryllium Oxide (BeO) and therefore It must not be ground, machined, sandblasted or subjected to any mechanical operation which will produce dust. The case must not be subjected to any chemical process (e.g. etching) which will produce fumes.

&TO-254AA) PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION Metal Flange Mount Package (TO-254)







01,03,05,

1.6

1.7



Symbols	Dimensio	ons mm	Notes
Symbols	Min	Max	
A	13.59	13.84	
В	13.59	13.84	
С	20.07	20.32	
D	6.3	6.7	, , , , , , , , , , , , , , , , , , ,
E-	1	1.35	
ØF	3.5	3.9	
G	16.89	17.4	
Н	6.86	BSC	
ØI	0.89	1.14	2
J	3.81	BSC	
K	3.81	BSC	
L	12.95	14.5	
ØM	3.05 T	ypical	2
N	- 0.71		2
R1	_	1	3
R2	1.65 T	ypical	4

NOTES:

- The terminal identification is specified by the components geometry. See Functional Diagram for the terminal connections.
- 2. 3 places.
- 3. Radius of heatsink flange corner, 4 places.
- 4. Radius of body corner, 4 places.

1.8 FUNCTIONAL DIAGRAM

Varients01,02,03

Terminal 1: Cathode

Terminal 2: Not connected 3 —

3

Terminal 3: Anode

NOTES:

Add new figure-see attached)

1. The case is not connected to any lead.

1.9 MATERIALS AND FINISHES

Materials and finishes shall be as follows:

a) Case

The case shall be hermetically sealed and have a metal body. The leads pass through ceramic eyelets brazed into the frame and the lid shall be welded.

X

Variants 04,05

Termind 1: Cathode Terminal 2: Amode Terminal 3: Anode $\begin{bmatrix} 2 \\ 3 \end{bmatrix}$



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b) Leads As specified in Component Type Variants.

REQUIREMENTS 2.

GENERAL 2.1

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

Deviations from the Generic Specification 2.1.1

Deviations from Qualification and Periodic Tests - Chart F4 2.1.1.1

(a) Constant Acceleration is not applicable.

2.2 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows.

The information to be marked on the component shall be:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- The ESCC Component Number. (b)
- (c) Traceability information.
- (d) Warning sign for Berryllium Oxide.

2.3 TERMINAL STRENGTH

The test conditions for Terminal Strength, tested as specified in the ESCC Generic Specification, shall be as follows:

Test Condition: A, tension, with an applied force of 10N for a duration of 10s.

ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES 2.4

Electrical measurements shall be performed at room, high and low temperatures.

Consolidated notes are given after the Tables.

2.4.1 Room Temperature Electrical Measurements

The measurements shall be performed at T_{amb} =+22 ±3°C.

*	(see
	changes 1
	changes as attacked)

Characteristics	Symbols	MIL-STD-750	Test Conditions	Limits		Units
		Test Method	(Note 1)	Min	Max	
Reverse Current	I _R	4016	DC Method V _R = 200V	-	50	μА
Forward Voltage	V _{F1}	4011 *	Pulse Method I _F =20A, Noteオス	-	1.1 0.95	V
	V _{F2}	4011	Pulse Method I _F =30A, Note #2	-	1.3 l·\	V
Breakdown Voltage	V _(BR)	4021	I _R = 100μA	200	-	V
Capacitance	С	4001	V _R = 10V f = 1MHz	-	400	pF
Reverse Recovery Time	t _{rr}	4031	Test Condition A I _F =1A V _B = 30V dI _F /dt=-50A/μs	-	60	ns
Thermal Impedance, Junction to Case	Z _{th(j-c)}	3101	I _H = 15V to 40A t _H = 50ms I _M = 50mA t _{md} = 100μs Note 2/3	(Calcula see N	ate ∆V _F , ote 3) 4 -	°C/W

2.4.2 <u>High and Low Temperatures Electrical Measurements</u>

* (See changes as ottained)

*

Characteristics	Symbols	MIL-STD-750	Test Conditions	Lin	Units	
		Test Method	(Note #1 ,5)	Min	Max	May Val
Reverse Current	l ^B	4016	T _{case} =+125(+0 -5)°C DC Method V _R = 200V	-	40	mA
Forward Voltage	V _{F1}	4011 *	T _{case} =+125(+0 -5)°C Pulse Method I _F =20A, Note +2	7	0.85	٧
		*	T _{case} =-55(+5 -0)°C Pulse Method I _F =20A, Note 12	₩	1.3 I·15	٧
	V _{F2}	4011 *	T_{case} =+125(+0 -5)°C Pulse Method I_F = 30A, Note $1/2$	PP-	1.1 1	٧

2.4.3 Notes to Electrical Measurement Tables

- 2 /r. Pulsed Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3 2. Performed only during Screening Tests Parameter Drift Values (Initial Measurements), go-no-go.
- 4.8. The limits for ΔV_F shall be defined by the manufacturer on every lot in accordance with MIL-STD-750 Method 3101 and shall guarantee the R_{th(j-c)} limits specified in Maximum Ratings.
- 5 A. Read and record measurements shall be performed on a sample of 5 components with 0 failures
- 1. For Variants 04,05 testing shall be performed with both anode terminals 2 and 3 tied together

Para 2.4.1 Room Temperature Electrical Measurements

Characteristics	Symbols	MIL-STD-750	Test Conditions	Lir	nits	Units
		Test Method	(Note 1)	Min	Max	
Forward Voltage 1	$V_{\rm F1}$	4011	Pulse Method	-		V
	• •		I _F = 20 A, Note 2			
			Variants 01, 02, 03		1.1	
			Variants 04, 05		0.95	
Forward Voltage 2	$V_{\rm F2}$	4011	Pulse Method	T -		V
1012111			$I_F = 30 A$, Note 2			
			Variant 01 to 03		1.3	
			Variant 04 and 05	<u></u>	1.1	

Para 2.4.2 High and Low Temperature Electrical Measurements

Characteristics	Symbols	MIL-STD-750	Test Conditions	Lin	nits	Units
		Test Method	(Notes 1, 5)	Min	Max	
Forward Voltage 1	v_{F1}	4011	$T_{\text{case}} = +125(+0.5)^{\circ}\text{C}$	-		V
			Pulse Method			
			I _F = 20 A, Note 2 Variants 01, 02, 03 Variants 04, 05		1 0.85	
			T _{case} =-55(+5 -0)°C Pulse Method			V
			I _F = 20 A, Note 2 Variants 01, 02, 03 Variants 04, 05		1.3 1.15	
Forward Voltage 2	V_{F2}	4011	$T_{case} = +125(+0.5)^{\circ}C$	-		V
			Pulse Method			
			I _F = 30 A, Note 2 Variants 01, 02, 03 Variants 04, 05		1.1 1	



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allowed. Alternatively a 100% inspection may be performed.

2.5 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ± 3 °C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

Characteristics	Symbols		Limits		Units
		Drift	Abs	olute	
		Value ∆	Min	Max	
Reverse Current	I _R	±6 or (1) ±100%	-	50	μA
Forward Voltage 1	V _{F1}	±0.07	-	1.1	٧
ES:					0.95

* See chruses as attached

2.6

NOTES:

INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 $\pm 3^{o}$ C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements . The limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Lin	nits	Units
		Min	Max	
Reverse Current	I _R	_	50	μΑ
Forward Voltage 1	V _{F1}	-	1	V
K <	VF2	(XL)	78	*

Forward Voltage 1

VF1

And VF2

Forward Voltage 1

VF1

VF2

VF2

VF2

And VF2

And

Characteristics	Symbols	Limits	Units
Ambient Temperature	T _{amb}	+150 (+0 -5)	°C
Reverse Voltage	V _R	160	٧
Duration	t	≥48	hours

Nones:

1. For Variants 04,05 testing shall be performed with both anode terminals 2 and 3 field together.

^{1.} Whichever is the greater referred to the initial value.

Para 2.5 PARAMETER DRIFT VALUES

Characteristics	Symbols	Limits			Units
		Drift Value		olute	
		Value Δ	Min	Max	
Forward Voltage 1 Variants 01, 02, 03 Variants 04, 05	V _{F1}	± 0.07	-	1.1 0.95	V

Para 2.6 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Characteristics	Symbols	Limits		Units
- Anna Caraca Ca		Min	Max	
Forward Voltage 1	VF1	<u> </u>		V
Variants 01, 02, 03	,,,,		1.1	,
Variants 04, 05			0.95	
Forward Voltage 2	VF2	-		V
Variants 01, 02, 03			1.3	
Variants 04, 05		***************************************	1.1	



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2.8 POWER BURN-IN CONDITIONS

Characteristics	Symbols	Test Conditions	Units
Case Temperature	T _{case}	+125 ±15, Note 1	°C
Junction Temperature	T _j	+150 (+0 -5)	°C
Average Output Rectified Current	lo	≥ 10, Note 1	Α

NOTES:

- 1. The case temperature and/or output current may be adjusted, within their given limit ranges, to attain the specified junction temperature.
- 2. For Variants 04,05 testing shall be performed with both amode terminals 2 and 3 tied together.

2.9 <u>OPERATING LIFE CONDITIONS</u>

The conditions shall be as specified for Power Burn-in.



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APPENDIX 'A'

AGREED DEVIATIONS FOR STMICROELECTRONICS (F)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
Deviations from Production Control- Chart F2	Special In-process Control Internal Visual Inspection. Wedge bonds equal to 1.1 wire diameters are acceptable for bonding with a V-Groove tool.