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TRANSISTORS, HIGH VOLTAGE, NPN

BASED ON TYPE 2N3439 AND 2N3440

ESCC Detail Specification No. 5203/011

| Issue 2 - Draft A | July 2007 |
|-------------------|-----------|
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ISSUE 2 - Draft A



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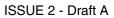




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1. GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 5000
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 <u>The ESCC Component Number</u>

The ESCC Component Number shall be constituted as follows:

Example: 520301101

• Detail Specification Reference: 5203011

Component Type Variant Number: 01 (as required)

1.4.2 <u>Component Type Variants</u>

The component type variants applicable to this specification are as follows:

| Variant Number | Based on Type | Case | Lead/Terminal Material and Finish | Weight max g |
|-------------------|---------------|-------|-----------------------------------|-----------------|
| 01 | 2N3439 | TO-5 | D2 | 1.5 |
| 02 | 2N3439 | TO-39 | D2 | 1.5 |
| 03 | 2N3439 | TO-5 | D3 or D4 | 1.5 |
| 04 | 2N3439 | TO-39 | D3 or D4 | 1.5 |
| 05 | 2N3440 | TO-5 | D2 | 1.5 |
| 06 | 2N3440 | TO-39 | D2 | 1.5 |
| 07 | 2N3440 | TO-5 | D3 or D4 | 1.5 |
| 08 | 2N3440 | TO-39 | D3 or D4 | 1.5 |

The lead/terminal material and finish shall be in accordance with the requirements of ESCC Basic Specification No. 23500.



MAXIMUM RATINGS 1.5

The maximum ratings shall not be exceeded at any time during use or storage. Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

| Characteristics | Symbols | Maximum Ratings | Unit | Remarks |
|---|-------------------|-----------------|------|--|
| Collector-Base Voltage Variants 01 to 04 Variants 05 to 08 | V _{CBO} | 450 300 | V | Over T _{op} |
| Collector-Emitter Voltage Variants 01 to 04 Variants 05 to 08 | V _{CEO} | 350 250 | V | Over T _{op} |
| Emitter-Base Voltage | V _{EBO} | 7 | V | Over T _{op} |
| Collector Current | IC | 1 | Α | |
| Power Dissipation | P _{tot1} | 800 | mW | At T _{amb} ≤ +25°C Note 1 |
| | P _{tot2} | 5 | W | At T _{case} ≤ +25°C Note 1 |
| Operating Temperature Range | T _{op} | -65 to +200 | °C | Note 2 |
| Storage Temperature Range | T _{stg} | -65 to +200 | °C | Note 2 |
| Soldering Temperature | T _{sol} | +260 | °C | Note 3 |

NOTES:

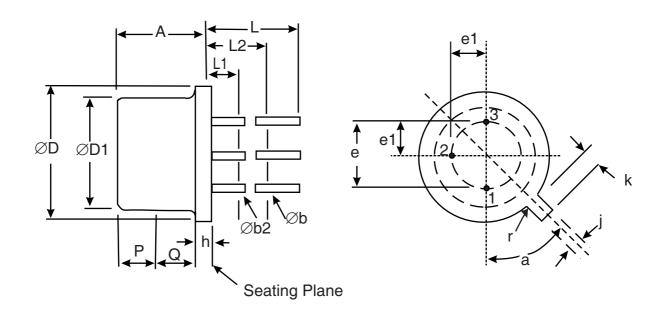
- For T_{amb} or $T_{case} > +25^{\circ}$ C, derate linearly to 0W at +200°C. For Variants with tin-lead plating or hot solder dip lead finish all testing performed at $T_{amb} > +125^{\circ}$ C shall be carried out in a 100% inert atmosphere.
- Duration 10 seconds maximum at a distance of not less than 1.5mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.



1.6

PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION

1.6.1 <u>Metal Can Package (TO-5) - 3 lead</u>

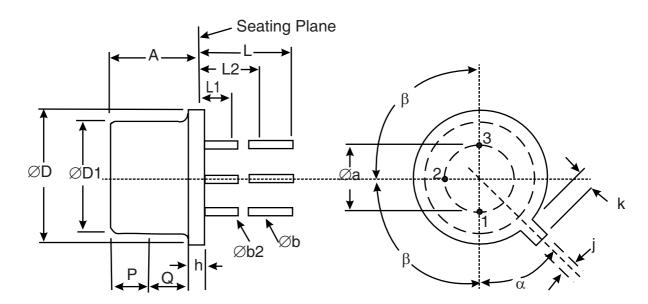


| Symbols | Dimensio | Notes | |
|---------|----------|-------|-------|
| Cymbols | Min | Max | Notes |
| Α | 6.1 | 6.6 | |
| Øb | 0.406 | 0.533 | 2, 3 |
| Øb2 | 0.406 | 0.483 | 2, 3 |
| ØD | 8.51 | 9.4 | |
| ØD1 | 7.75 | 8.51 | |
| е | 5.08 | BSC | 6 |
| e1 | 2.54 BSC | | |
| h | 0.229 | 3.18 | |
| j | 0.711 | 0.864 | |
| k | 0.737 | 1.14 | 4 |
| L | 38.1 | - | 2 |
| L1 | - | 1.27 | 2, 3 |
| L2 | 6.35 | - | 2, 3 |
| Р | 2.54 | - | 5 |
| Q | - | - | 7 |
| r | - | 0.179 | 8 |
| а | 45° E | BSC | 1,9 |



- 1. Terminal identification is specified by reference to the tab position where Lead 1 = emitter, Lead 2 = base and Lead 3 = collector.
- 2. Applies to all leads.
- 3. Øb2 applies between L1 and L2. Øb applies between L2 and 38.1mm from the seating plane. Diameter is uncontrolled within L1 and beyond 38.1mm from the seating plane.
- 4. Measured from the maximum diameter of the actual device.
- 5. This zone is controlled for automatic handling. The variation in actual diameter within this zone shall not exceed 0.254mm.
- 6. Leads having maximum diameter 0.483mm measured in the gauging plane 1.37 (+0.025, -0)mm below the seating plane of the device shall be within 0.178mm of their true position relative to a maximum-width-tab.
- 7. The details of outline in this zone are optional.
- 8. Radius.
- 9. Measured from the tab centreline.

1.6.2 Metal Can Package (TO-39) - 3 lead



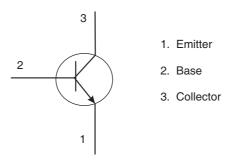
| Symbols | Dimension | Notes | |
|---------|-----------|-------|-------|
| Symbols | Min | Max | Notes |
| Øa | 4.83 | 5.35 | |
| Α | 6 | 6.6 | |
| Øb | 0.4 | 0.533 | 2, 3 |
| Øb2 | 0.4 | 0.483 | 2, 3 |
| ØD | 8.31 | 9.4 | |
| ØD1 | 7.75 | 8.51 | 5 |
| h | 0.229 | 3.18 | |
| j | 0.711 | 0.864 | |



| Symbols | Dimension | Notes | |
|---------|-----------|-------|-------|
| Symbols | Min | Max | Notes |
| k | 0.737 | 1.14 | 4 |
| L | 12.7 | 19 | 2 |
| L1 | - | 1.27 | 2, 3 |
| L2 | 6.35 | - | 2, 3 |
| Р | 2.54 | - | 5 |
| Q | - | - | 6 |
| α | 45° E | 1, 7 | |
| β | 90° E | BSC | 1 |

- 1. Terminal identification is specified by reference to the tab position where Lead 1 = emitter, Lead 2 = base and Lead 3 = collector.
- 2. Applies to all leads.
- 3. Øb2 applies between L1 and L2. Øb applies between L2 and 12.7mm from the seating plane. Diameter is uncontrolled within L1 and beyond 12.7mm from the seating plane.
- 4. Measured from the maximum diameter of the actual device.
- 5. This zone is controlled for automatic handling. The variation in actual diameter within this zone shall not exceed 0.254mm.
- 6. The details of outline in this zone are optional.
- 7. Measured from the tab centreline.

1.7 FUNCTIONAL DIAGRAM



NOTES:

1. The collector is internally connected to the case.

1.8 <u>MATERIALS AND FINISHES</u>

Materials and finishes shall be as follows:

- a) Case
 - The case shall be hermetically sealed and have a metal body with hard glass seals.
- b) Leads/Terminals



As specified in Component Type Variants.

2. REQUIREMENTS

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

2.1.1.1 Deviation from Screening Tests - Chart F3

High Temperature Reverse Bias Burn-in and the subsequent Final Measurements for HTRB shall be omitted.

2.2 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows

The information to be marked on the component shall be:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.
- (c) Traceability information.

2.3 <u>TERMINAL ST</u>RENGTH

The test conditions for terminal strength, tested as specified in the ESCC Generic Specification, shall be as follows:

Test Condition: E, lead fatigue.

2.4 <u>ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES</u>

Electrical measurements shall be performed at room, high and low temperatures.

2.4.1 Room Temperature Electrical Measurements

The measurements shall be performed at T_{amb} =+22 ±3°C.

| Characteristics | Symbols | | Test Conditions | Lin | nits | Units |
|--|----------------------|-------------|--|------------|--------|-------|
| | | Test Method | | Min | Max | |
| Collector-Base Breakdown Voltage | V _{(BR)CBO} | 3001 | I _C = 100μA Bias condition D Variants 01 to 04 Variants 05 to 08 | 450 300 | - - | V |

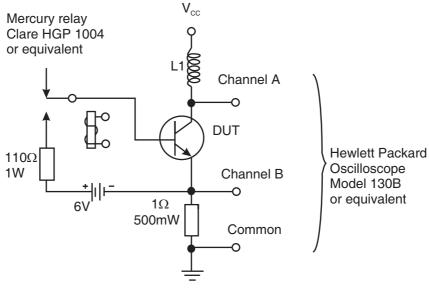


| Characteristics | Symbols | MIL-STD-750 | Test Conditions | Lin | nits | Units |
|--|-----------------------|-------------|--|------------|------|-------|
| | | Test Method | | Min | Max | |
| Collector-Emitter Sustaining Voltage | V _{CEO(sus)} | - | I _B = 5mA Variants 01 to 04 Variants 05 to 08 Note 4 | 350 250 | | V |
| Emitter-Base Breakdown Voltage | V _{(BR)EBO} | 3026 | I _B =1mA Bias condition D | 7 | - | V |
| Collector-Emitter Cut-off Current | I _{CEX} | 3041 | Bias condition A Variants 01 to 04: V _{CE} =450V, V _{BE} =-1.5V Variants 05 to 08: V _{CE} =300V, V _{BE} =-1.5V | - | 500 | μΑ |
| Collector-Base Cut-off Current | I _{CBO} | 3036 | Bias condition D Variants 01 to 04: V _{CB} =360V Variants 05 to 08: V _{CB} =250V | - | 20 | μΑ |
| Emitter-Base Cut- off Current | I _{EBO} | 3061 | V _{EB} =6V Bias condition D | - | 20 | μΑ |
| Forward-Current Transfer Ratio | h _{FE1} | 3076 | V _{CE} =10V, I _C =20mA Note 1 | 40 | 160 | - |
| | h _{FE2} | 3076 | V _{CE} =10V, I _C =2mA Note 1 | 30 | - | - |
| Collector-Emitter Saturation Voltage | V _{CE(sat)} | 3071 | I _C =50mA, I _B =4mA Notes 1, 2 | - | 500 | mV |
| Base-Emitter Saturation Voltage | V _{BE(sat)} | 3066 | I _C =50mA, I _B =4mA Test Condition A Notes 1, 2 | - | 1.3 | V |
| Magnitude of Small-Signal Short-Circuit Forward-Current Transfer Ratio | lh _{fe} l | 3306 | V _{CE} =10V I _C =10mA f=5MHz Note 3 | 3 | 15 | - |
| Output Capacitance | C _{obo} | 3236 | V _{CB} =10V I _E =0A f=1MHz Note 3 | - | 10 | pF |

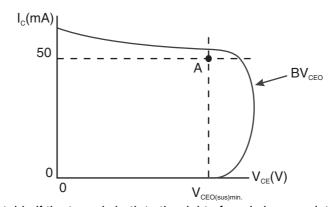
- 1. Pulsed measurement: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 2. Saturation voltage is measured 6mm from the case.
- 3. For AC characteristics read and record measurements shall be performed on a sample of 32 components with 0 failures allowed. Alternatively a 100% inspection may be performed.
- 4. $V_{CEO(sus)}$ shall be measured using the test circuit shown below. $V_{CC} = 0V$ to 60V, pulsed, Pulse



Width \leq 300 μs , Duty Cycle \leq 2%. I $_{CC}$ =100 mA. Inductor L1 shall have the following characteristics: L = 25 mH, Q = 76, R = 83.4 Ω , I = 75 mA.



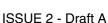
OSCILLOSCOPE DISPLAY



 BV_CEO is acceptable if the trace is both to the right of, and above, point A, as shown.

2.4.2 <u>High and Low Temperatures Electrical Measurements</u>

| | MIL-STD-750 | Test Conditions | Limits | | Units | |
|-----------------------------------|------------------|-----------------|--|-----|-------|----|
| | | Test Method | Test Method Note 1 | Min | Max | |
| Collector-Base Cut-off Current | І _{СВО} | 3036 | T _{amb} =+150(+0-5)°C Bias condition D Variants 01 to 04: V _{CB} =360V Variants 05 to 08: V _{CB} =250V | - | 500 | μА |





| Characteristics | Symbols | MIL-STD-750 Test Conditions Note 1 | | Lin | nits | Units |
|-------------------------------------|-----------------------|------------------------------------|--|-----|------|-------|
| | l est Method Note I | | l'est Method IN | Min | Max | |
| Forward-Current Transfer Ratio 1 | h _{FE1} | 3076 | T _{amb} =-55(+5-0)°C V _{CE} =10V, I _C =20mA Note 2 | 20 | - | - |

- 1. Read and record measurements shall be performed on a sample of 5 components with 0 failures allowed. Alternatively a 100% inspection may be performed.
- 2. Pulsed measurement: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.

2.5 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 $\pm 3^{\circ}$ C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

| Characteristics | Symbols | Limits | | | Units |
|--------------------------------------|----------------------|------------|----------|-----|-------|
| | | Drift | Absolute | | |
| | | Value Δ | Min | Max | |
| Collector-Base Cut-off Current | I _{CBO} | ±4 | - | 20 | μА |
| Forward-Current Transfer Ratio 1 | h _{FE1} | ±20% | 40 | 160 | - |
| Collector-Emitter Saturation Voltage | V _{CE(sat)} | ±50 | - | 500 | mV |

2.6 <u>INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS</u>

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The limit values for each characteristic shall not be exceeded.

| Characteristics | Symbols | Limits | | Units |
|--------------------------------------|----------------------|--------|-----|-------|
| | | Min | Max | |
| Collector-Base Cut-off Current | I _{CBO} | - | 20 | μΑ |
| Forward-Current Transfer Ratio 1 | h _{FE1} | 40 | 160 | - |
| Collector-Emitter Saturation Voltage | V _{CE(sat)} | - | 500 | mV |



2.7 <u>POWER BURN-IN CONDITIONS</u>

| Characteristics | Symbols | Test Conditions | Units |
|---|------------------|-----------------|-------|
| Ambient Temperature | T _{amb} | +25 ±3 | °C |
| Power Dissipation | P _{tot} | 800 | mW |
| Collector-Emitter Voltage Variants 01, 02, 03, 04 Variants 05, 06, 07, 08 | V _{CE} | 250 180 | V |

NOTES:

1. No heat sink nor forced air directly on the device shall be permitted.

2.8 <u>OPERATING LIFE CONDITIONS</u>

The conditions shall be as specified for Power Burn-in.



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APPENDIX 'A'

AGREED DEVIATIONS FOR STMICROELECTRONICS (F)

| ITEMS AFFECTED | DESCRIPTION OF DEVIATIONS |
|--|--|
| Deviations from Room Temperature Electrical Measurements | All AC characteristics (Room Temperature Electrical Measurement Note 3) may be considered guaranteed but not tested if successful pilot lot testing has been performed on the wafer lot which includes AC characteristic measurements per the Detail Specification. A summary of the pilot lot testing shall be provided if required by the Purchase Order. |
| Deviations from High and Low Temperatures Electrical Measurements | All characteristics specified may be considered guaranteed but not tested if successful pilot lot testing has been performed on the wafer lot which includes characteristic measurements at high and low temperatures per the Detail Specification. A summary of the pilot lot testing shall be provided if required by the Purchase Order. |