

**TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - a.c. PARAMETERS**

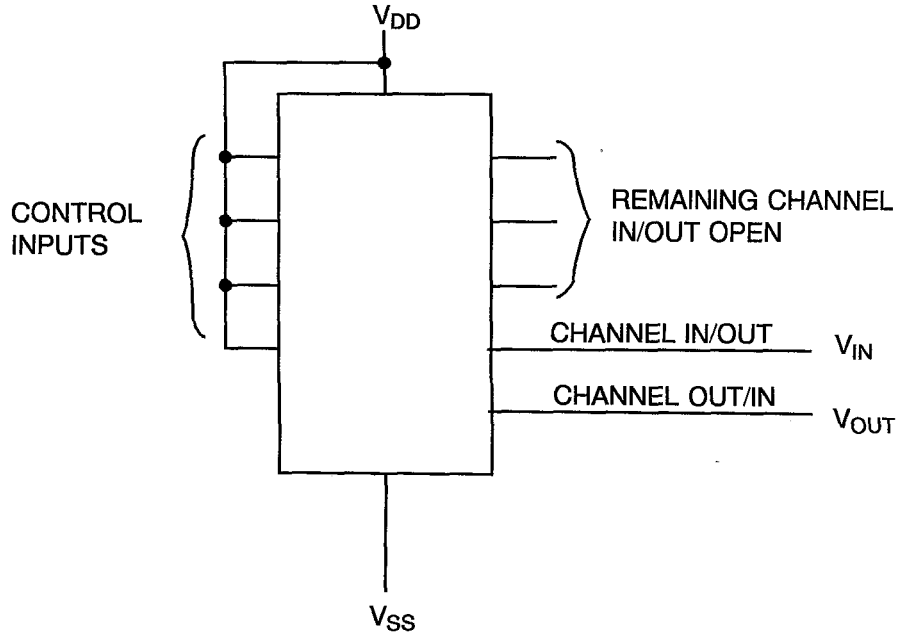
NO.	CHARACTERISTICS	SYMBOL	TEST METHOD MIL-STD 883	TEST FIG.	TEST CONDITIONS (PINS UNDER TEST D/F = DIP AND FP C = CCP)	LIMITS		UNIT
						MIN	MAX	
104 to 107	Input Capacitance (Control)	C_{IN}	3012	4(m)	V_{IN} (Not Under Test) = 0Vdc $V_{DD} = V_{SS} = 0Vdc$ Note 5 (Pins D/F 5-6-12-13) (Pins C 7-9-17-19)	-	7.5	pF
108 to 111	Channel Capacitance (Input)	C_{INC}	3012	4(n)	$V_{DD} = V_{SS} = 0Vdc$ Note 5 (Pins D/F 1-4-8-11) (Pins C 2-6-12-16)	-	15	pF
112 to 115	Channel Capacitance (Output)	C_{OC}	3012	4(o)	$V_{DD} = V_{SS} = 0Vdc$ Note 5 (Pins D/F 2-3-9-10) (Pins C 4-5-14-15)	-	15	pF
116	Propagation Delay Signal IN to Signal OUT (Channel turned ON)	t_{PLH1}	3003	4(p)	V_{IN} (Under Test) = Pulse Generator $V_{DD} = 5Vdc, V_{SS} = 0Vdc$ Note 6 <u>Pins D/F</u> <u>Pins C</u> 1 to 2 2 to 4	-	40	ns
117	Propagation Delay Signal IN to Signal OUT (Channel turned ON)	t_{PHL}	3003	4(p)	V_{IN} (Under Test) = Pulse Generator $V_{DD} = 5Vdc, V_{SS} = 0Vdc$ Note 6 <u>Pins D/F</u> <u>Pins C</u> 1 to 2 2 to 4	-	40	ns
118	Propagation Delay Time Control to Switch On	t_{PLH2}	3003	4(q)	V_{IN} (Under Test) = Pulse Generator $V_{DD} = 5Vdc, V_{SS} = 0Vdc$ Note 6 <u>Pins D/F</u> <u>Pins C</u> 13 to 2 19 to 4	-	70	ns

NOTES: See Page 23.

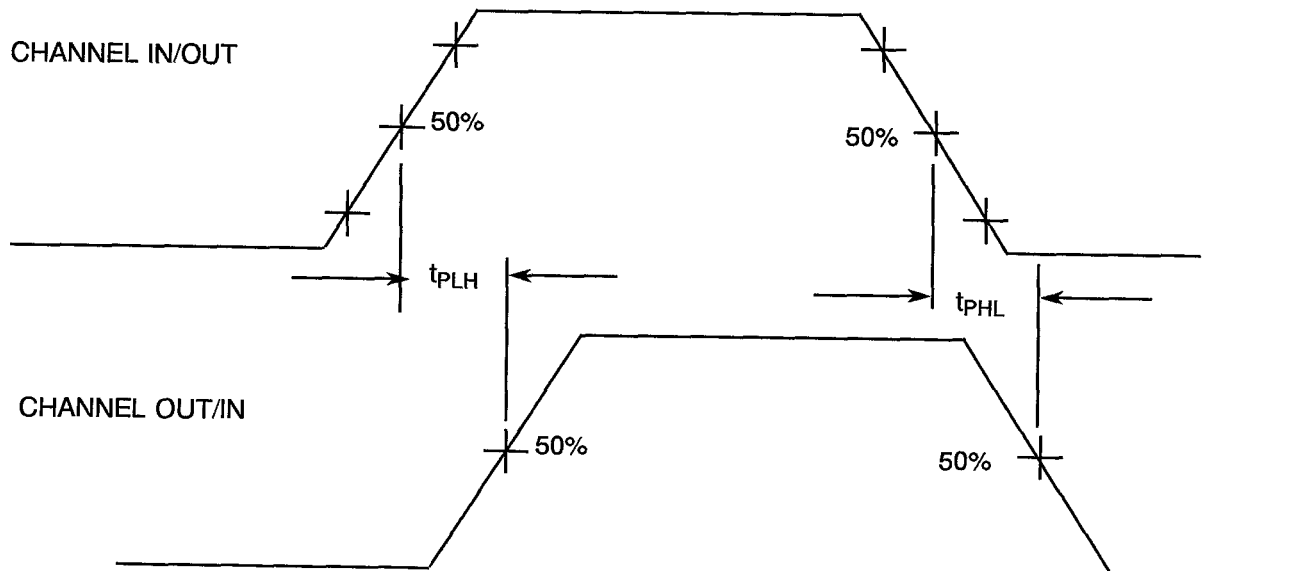


FIGURE 4 - CIRCUITS FOR ELECTRICAL MEASUREMENTS (CONTINUED)

FIGURE 4(p) - PROPAGATION DELAY SIGNAL IN TO SIGNAL OUT



VOLTAGE WAVEFORMS



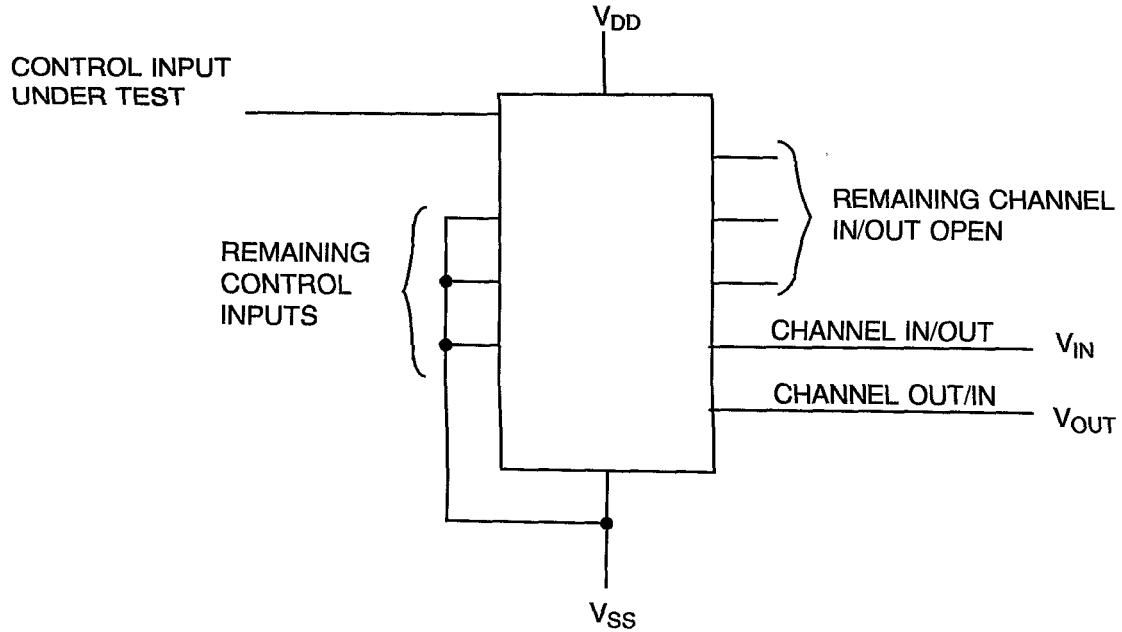
NOTES

- 1. Pulse Generator - $V_P = 0$ to V_{DD} , t_r and $t_f \leq 15ns$, $f = 500kHz$.

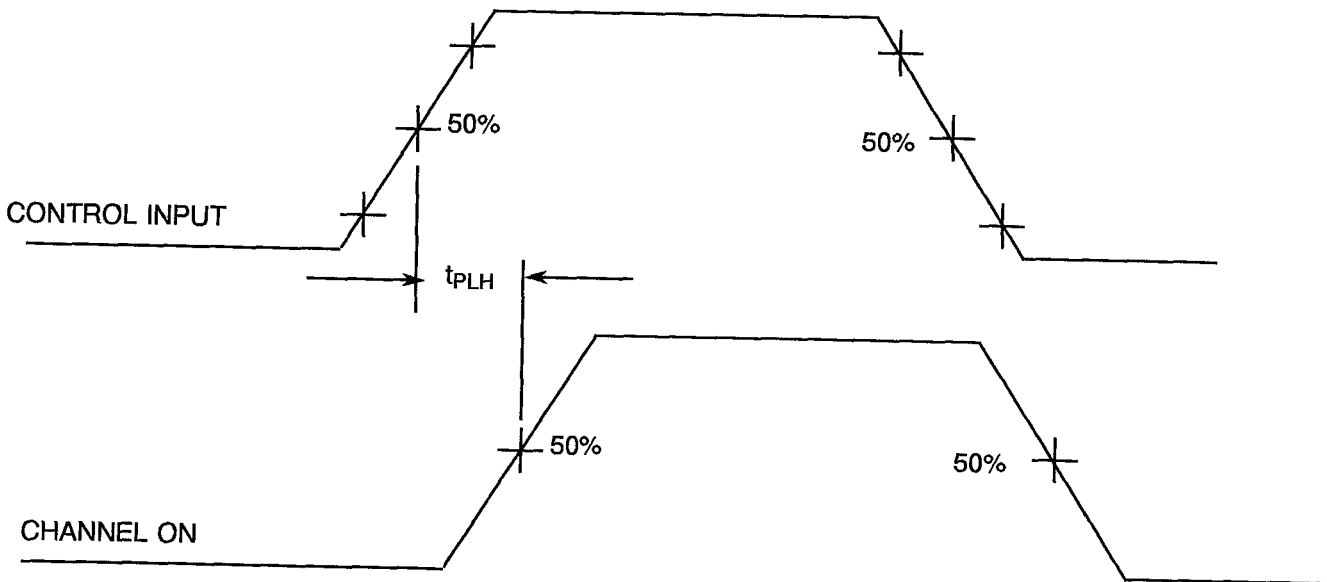


FIGURE 4 - CIRCUITS FOR ELECTRICAL MEASUREMENTS (CONTINUED)

FIGURE 4(q) - PROPAGATION DELAY, CONTROL TO SWITCH ON



VOLTAGE WAVEFORMS



NOTES

- 1. Pulse Generator - $V_P = 0$ to V_{DD} , t_r and $t_f \leq 15ns$, $f = 500kHz$.